Particle Detectors

Lecture 17
06/05/15

a.a. 2015-2016

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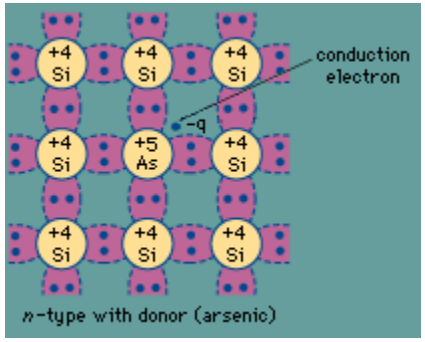
Rivelatori a stato solido

Drogaggio dei semiconduttori

Inserendo nel reticolo cristallino atomi opportuni e' possibile "modulare" le proprieta' elettriche dei semiconduttori

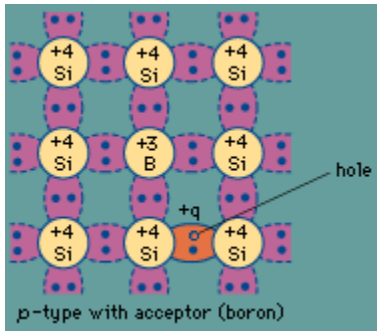
TIPO n



Aggiungendo al silicio impurità del V gruppo (Pentavalenti: antimonio, fosforo, arsenico) con 5 elettroni di valenza (donori) facciamo diventare il silicio di tipo n.

Gli elettroni sono portatori di maggioranza

TIPO p

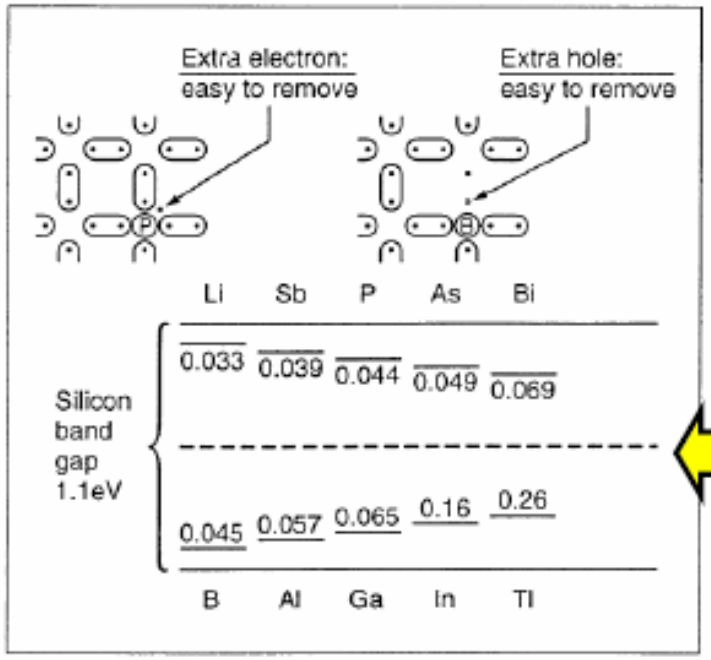


Aggiungendo impurità del III gruppo (Trivalenti: boro, gallio, indio) con tre elettroni di valenza (accettori), il silicio diventa di tipo p.

Le lacune sono portatori di maggioranza

Silicon doping

- Pure silicon has a very high resistance at room temp. (235 kOhm cm)
- Doping: A few silicon atoms can be replaced by atoms of an element of the 3rd main group (i.e. Boron) →p type, or of the 5th main group (i.e. Phosphorus) →n type.



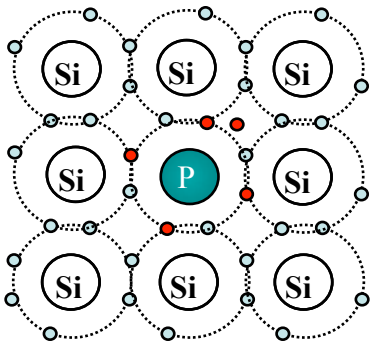
Typical doping concentration 10^{-11} !

n type: Fermi level in upper half
p type: in lower half

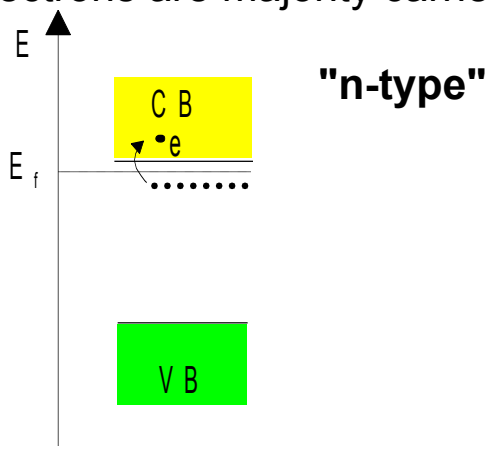
Important: Charge carriers only weakly bound.

Doping and resistivity

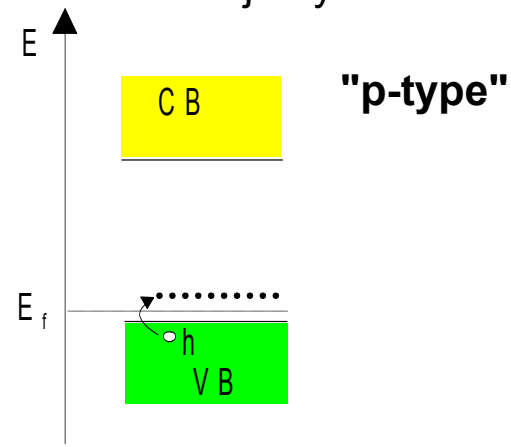
e.g. Phosphorus



- **Doping: n-type Silicon**
  - add elements from Vth group
    ⇒ **donors** (P, As,..)
  - electrons are majority carriers



- **Doping: p-type Silicon**
  - add elements from IIIrd group
    ⇒ **acceptors** (B,..)
  - holes are the majority carriers



- **Resistivity**
  - carrier concentrations *n*, *p*
  - carrier mobility μ_n, μ_p

$$\rho = 1 / q_0 \left(\mu_n n + \mu_p p \right)$$

- Energy levels very close conduction bands.
- Energy difference < ~ thermal energy (3/2)kT = 40 meV
- At room T, donors and acceptor levels ionized → charge carriers in conduction band
- Electrical properties determined mainly by doping

Rivelatori a stato solido

In un semiconduttore in cui le impurità sono uniformemente distribuite, la densità di carica netta δq in un elemento di volume δV è =0. Indicando con N_d la concentrazione di donori e con N_a quella di accettori si ha:

$$\frac{\delta q}{\delta V} = q\left(p - n + N_d - N_a\right) = 0$$

Mettendo a sistema con $pn=n_i^2$ si ha

$$n = \frac{1}{2}\left(N_d - N_a + \sqrt{\left(N_d - N_a\right)^2 + 4n_i^2}\right)$$

$$p = \frac{1}{2}\left(N_a - N_d + \sqrt{\left(N_a - N_d\right)^2 + 4n_i^2}\right)$$

Tipicamente $N_a - N_d >> n_i^2$

Doping:

Tipo n: $N_d >> N_a$

$$n = N_d - N_a \approx N_d \quad \Rightarrow \quad \rho = \frac{1}{q\mu_n N_d}$$

Tipo p: $N_a >> N_d$

$$p = N_a - N_d \approx N_a \quad \Rightarrow \quad \rho = \frac{1}{q\mu_p N_a}$$

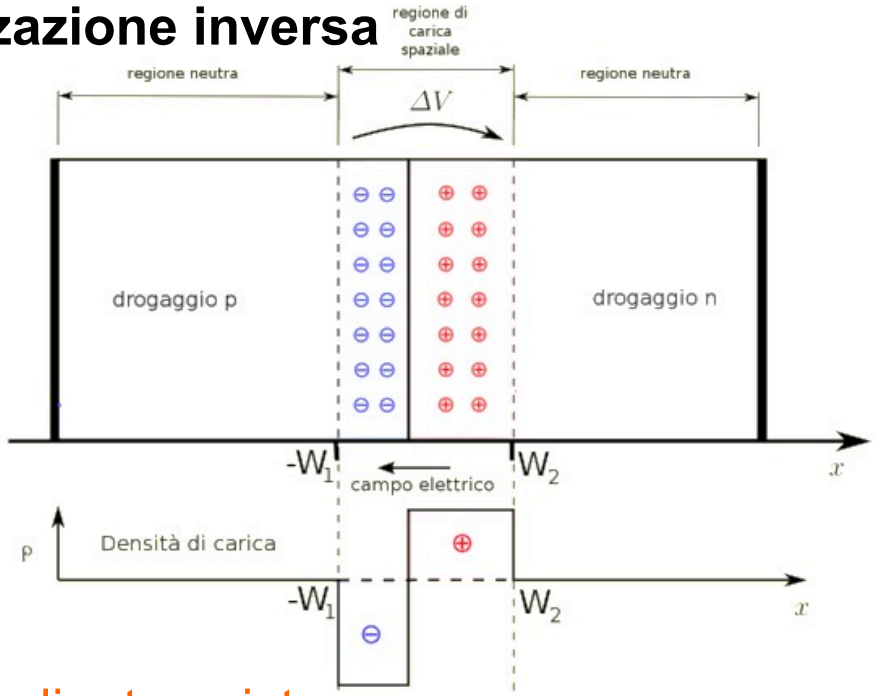
	detector grade	electronics grade
doping	$\approx 10^{12}$ cm^{-3}	$\approx 10^{17}$ cm^{-3}
resistivity ρ	$\approx$ 5 kΩ·cm	$\approx$1 Ω·cm

La resistivita' e' un parametro importante del silicio. Da essa dipendono parametri operativi come la tensione di lavoro, efficienza di raccolta di carica, rumore dell'apparato

Rivelatori a stato solido

La giunzione p-n.

Abbiamo visto che occorre ridurre la carica libera nel volume attivo per ottenere un rapporto fra segnale e rumore favorevole (24000 coppie di segnale vs 1.5×10^{10} di coppie termiche) occorre svuotare di carica libera il volume attivo→ **diodo in polarizzazione inversa**



Date 2 regioni contigue di silicio una di tipo p e l'altra di tipo n.

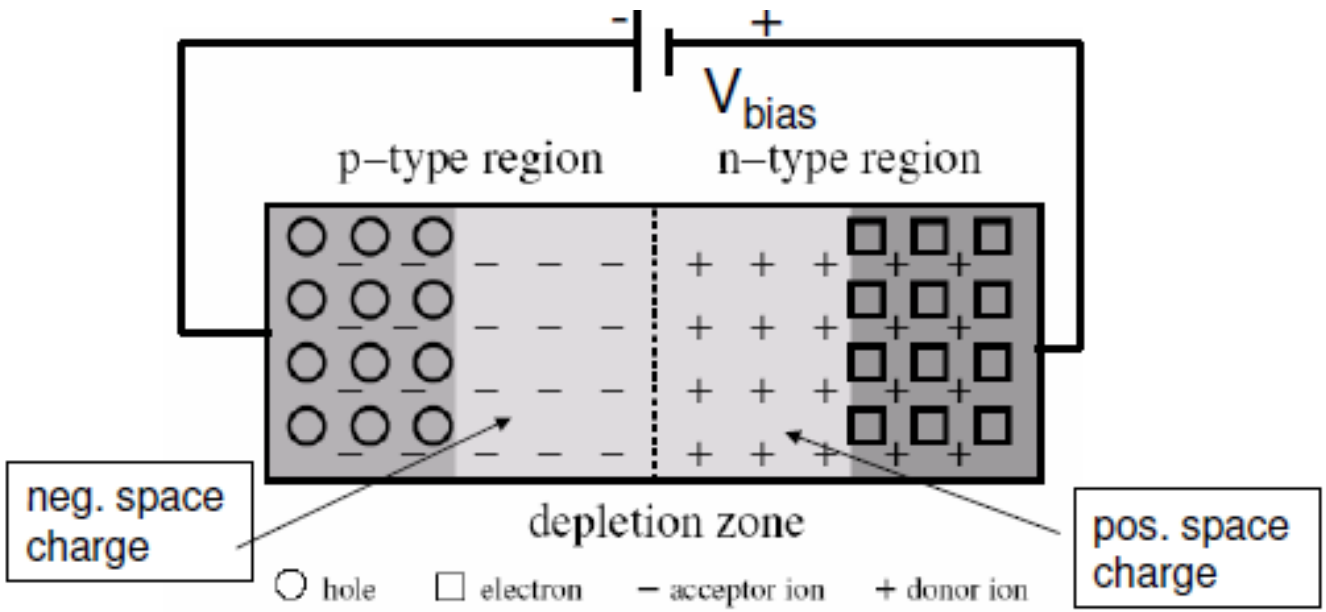
La giunzione e' costituita dall'interfaccia tra la regione p ed n, in cui la densita' di carica cambia bruscamente

Valori tipici di drogaggio sono:

$10^{12}/cm^3$ (n) e $10^{15}/cm^3$ (p)

(molto minori che nei circuiti integrati e diodi o transistor, nei quali la concentrazione è ~ $10^{17(18)}/cm^3$)

P-N junction



electrons drift towards p-side, holes towards n-side → buildup of a potential.

Without an external potential applied, there will be a thermal flux of charge carriers at the junction: a flux of e- from n to p region and h from p to n region.
A space charge region forms due to fixed positive ions in the n region and fixed e- in the p region.
Net flux is = 0 when the built-up electric field, or the contact potential V_c due the space charge stops further diffusion

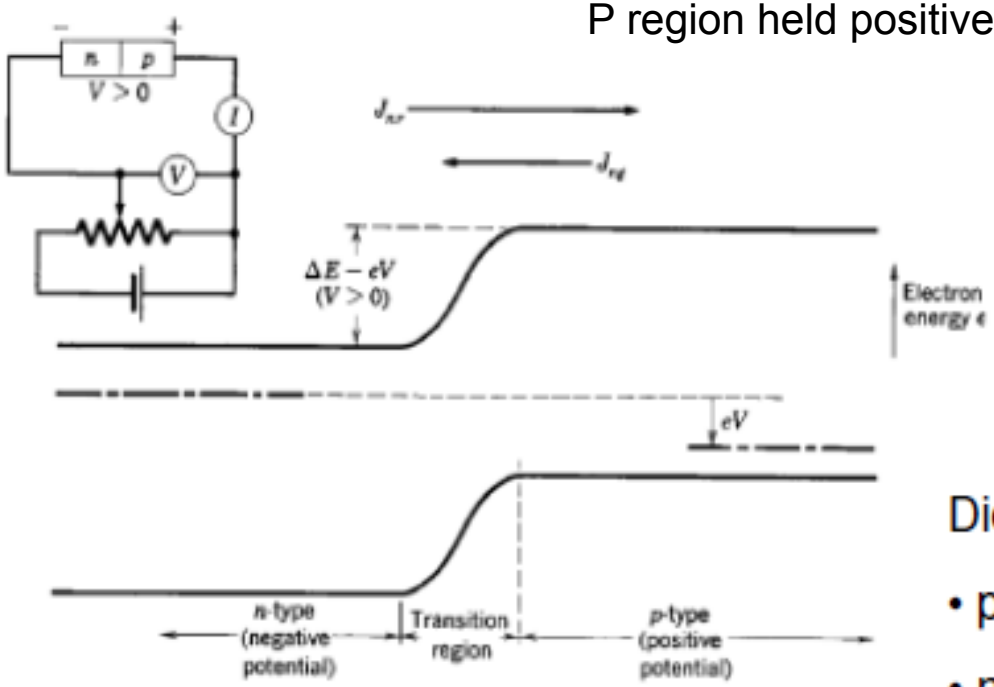
$$V_d = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2} \approx 600 \qquad mV$$

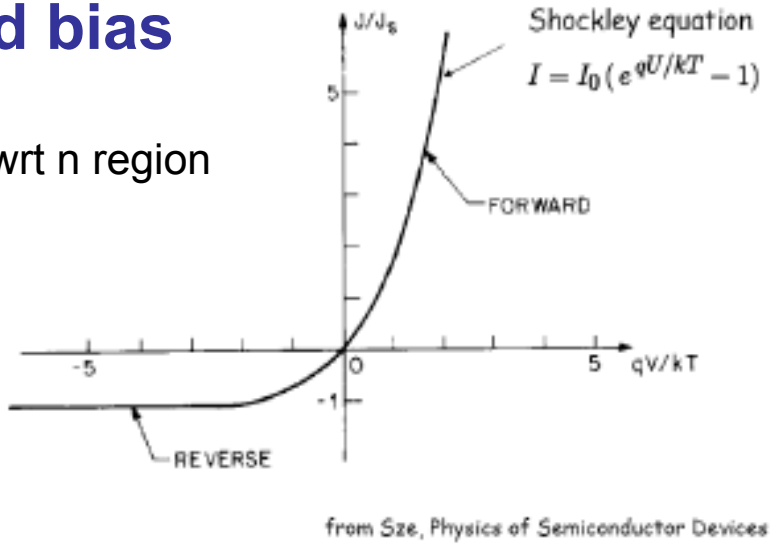
Correnti alla giunzione

- Per es. prendiamo gli elettroni. All'equilibrio, non c'e corrente netta.
- All'equilibrio, nella zona n ci sono cariche con energia termica sufficiente a superare la barriera di potenziale V_d ($n \sim \exp(-eV_d/2kT)$) e diffondere nella zona p, dove si ricombinano→flusso di ricomb. o di saturazione da n a p, J_{nr}
- Deve esserci flusso di cariche dalla zona p a quella n, generate termicamente nella zona p che diffondono attraverso la giunzione, J_{ng}.
- All'equilibrio $J_{ng} + J_{nr} = 0$
- Lo stesso argomento si applica alle lacune: $J_{pg} + J_{pr} = 0$
- Quando un pot. V esterno e' applicato, l'altezza della barriera di pot alla giunzione diventa $V_d + V$

P-N junction in forward bias

P region held positive wrt n region





Diode in Forward Bias

- positive potential at p-region
- negative potential an n-region
- The external voltage reduces the potential barrier.

→electrons from the n-region can cross the barrier.

In tal caso il # di e- che supera la barriera e' $\propto$ exp[-e(V_d – V)/kT] = (cost.)exp[eV/kT], quindi la corrente di saturazione J_{nr} e J_{pr} $\propto$ exp[eV/kT].
Per V = 0, $J_{pr} = J_{pg}$ e $J_{nr} = J_{ng}$
→ **$J_{pr} = J_{pg}$exp[eV/kT]** e **$J_{nr} = J_{ng}$exp[eV/kT]**
La corrente di generazione e' indipendente dal pot V → la corrente totale e'
$J_{tot} = (J_{pg} + J_{ng})$(exp[eV/kT] – 1)

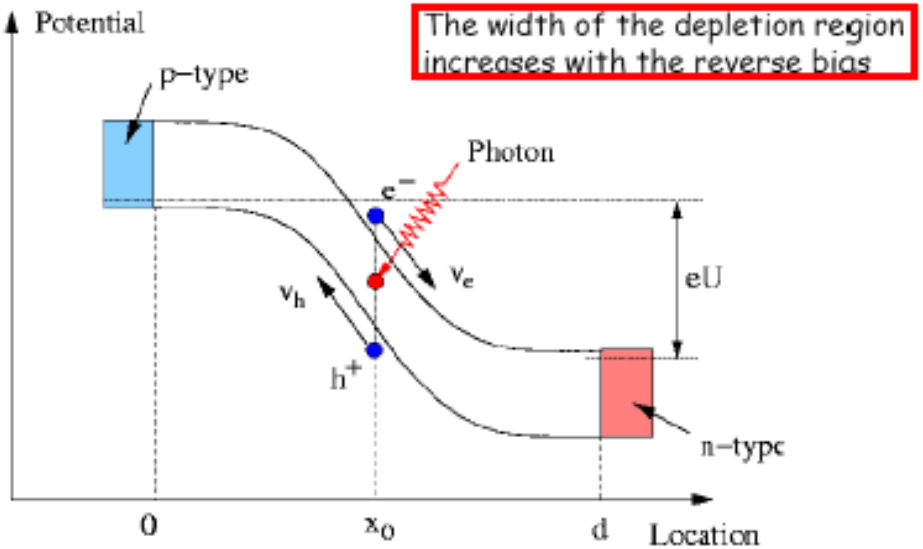
P-N junction in reverse bias

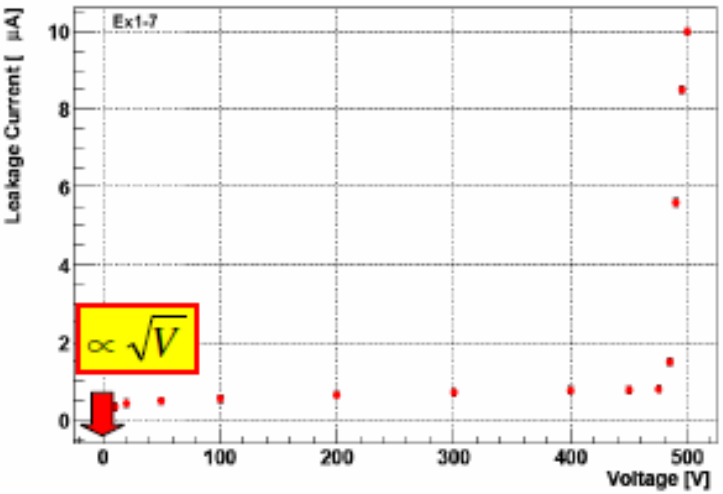
N positive wrt p

- The external voltage increases the potential barrier

- **The depletion zone can be used as detector, since it contains an electric field (and is depleted of free charges).**

Leakage current: Thermal generation of e h pairs → temperature dependant





Break through:

Detector behaves like a conductor (charge avalanche)

Se n e' tenuto positivo rispetto a p, la barriera di potenziale cresce di –eV → le correnti di saturazione sono soppresse dall'exp, mentre quelle di generazione rimangono costanti. Quindi in condizioni di "reverse" bias"

$J_{tot} = J_{pg} + J_{ng}$

La zona di carica spaziale alla giunzione si allarga

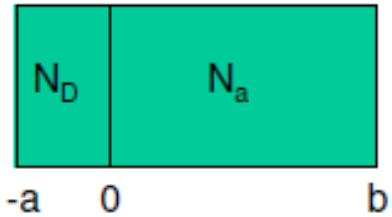
Nella zona di carica spaziale non ci sono cariche libere di muoversi

Depth of the depletion region

Poisson-Equation for the potential U(x) (1-dimensional for simplicity):

$$\frac{d^2U(x)}{dx^2} = \frac{-\rho(x)}{\varepsilon\varepsilon_0}$$

with $E_x = -dU/dx \rightarrow \frac{dEx(x)}{dx} = \frac{\rho(x)}{\varepsilon\varepsilon_0}$



$$\rho(x) = \begin{cases} eN_D & \text{für } -a < x \le 0 \\ -eN_A & \text{für } 0 < x \le b \end{cases}$$

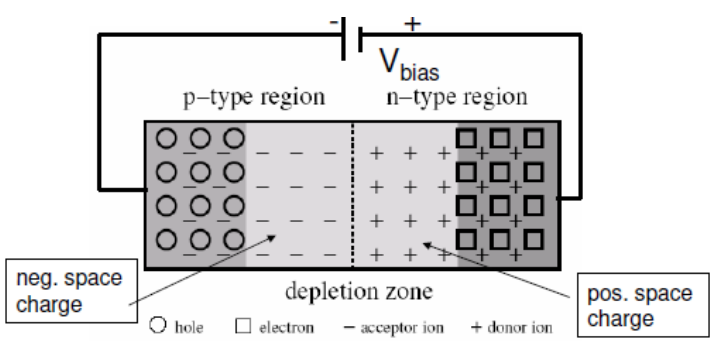
Asymmetric double layer with N_D, N_A density of donor- and acceptor impurities.

Assumption: $N_D >> N_A$ and a<b
Boundary conditions for electric field:

$$E_x(-a) = 0 = E_x(b)$$

1. Integration of Poisson equation with above boundary conditions

$$dU/dx = \begin{cases} -\frac{eN_D}{\varepsilon\varepsilon_0}(x+a) & \text{für } -a < x \le 0 \\ +\frac{eN_A}{\varepsilon\varepsilon_0}(x-b) & \text{für } 0 < x \le b \end{cases}$$



Depth of the depletion region

Boundary condition for the potential:

$$U(-a) = 0 \quad und \quad U(b) = -U_0$$ ←applied voltage

2. Integration:

$$U(x) = \begin{cases} -\dfrac{eN_D}{2\varepsilon\varepsilon_0}(x+a)^2 & für\ -a < x \le 0 \\ +\dfrac{eN_A}{2\varepsilon\varepsilon_0}(x-b)^2 - U_0 & für\ 0 < x \le b \end{cases}$$

Require continuity of potential and its derivative at the junction @ x=0

$$b(a+b) = \frac{2\varepsilon\varepsilon_0 U_0}{eN_A}$$

$$b \approx \left(\frac{2\varepsilon V}{qN_d\left(1 + {N_d}/{N_a}\right)} \right)^{1/2}$$

$$a \approx \left(\frac{2\varepsilon V}{qN_a\left(1 + {N_a}/{N_d}\right)} \right)^{1/2}$$

The highest field strength is then at x=0:

$$E_x(0) = \sqrt{\frac{2eN_A U_0}{\varepsilon_0 \varepsilon}} = \frac{2U_0}{d}$$

Rivelatori a stato solido

In assenza di potenziale esterno, l'altezza della barriera di potenziale di contatto V_d è :

$$V_d = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2} \approx 600 \quad mV$$

la zona di svuotamento è in genere piccola, circa 10 μm, con i valori tipici di doping

Se viene applicato un potenziale V, la zona di svuotamento diventa:

$$x_n = \left(\frac{2\varepsilon(V_d + V)}{qN_d\left(1 + N_d/N_a\right)} \right)^{1/2}$$

$$x_p = \left(\frac{2\varepsilon(V_d + V)}{qN_a\left(1 + N_a/N_d\right)} \right)^{1/2}$$

Se $N_a >> N_d$ $x_n >> x_p$ la zona di svuotamento è quasi tutta nella regione n (meno drogata):

$$d \approx x_n \approx \left(\frac{2\varepsilon(V_d + V)}{qN_d} \right)^{1/2} = \left(2\varepsilon\rho\mu V\right)^{1/2}$$

La tensione a cui lo spessore svuotato e' = a quello del rivelatore, d, e' il "depletion voltage"

$$V_{dep} \approx \frac{d^2}{2\varepsilon\rho\mu}$$

Il potenziale di contatto V_d e' di solito << V e puo' essere trascurato

In tal caso tutto il volume del diodo diventa "attivo", cioe' capace di dare un segnale utile.

→ **Fissa il punto di lavoro V > V_{dep}**

NB: si noti che piu' alta e' ρ, minore e' la tensione necessaria per svuotare il rivelatore

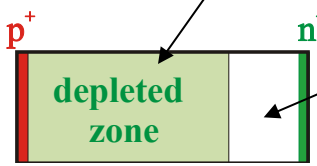
Valori tipici delle tensioni di svuotamento 50-100 V per 300 um di spessore

Reverse biased abrupt p^+ n junction

Poisson's equation

$$-\frac{d^2}{dx^2}\phi(x) = \frac{q_0}{\varepsilon\varepsilon_0}\cdot N_{eff}$$

Positive space charge, N_{eff} =[P]
(ionized Phosphorus atoms)

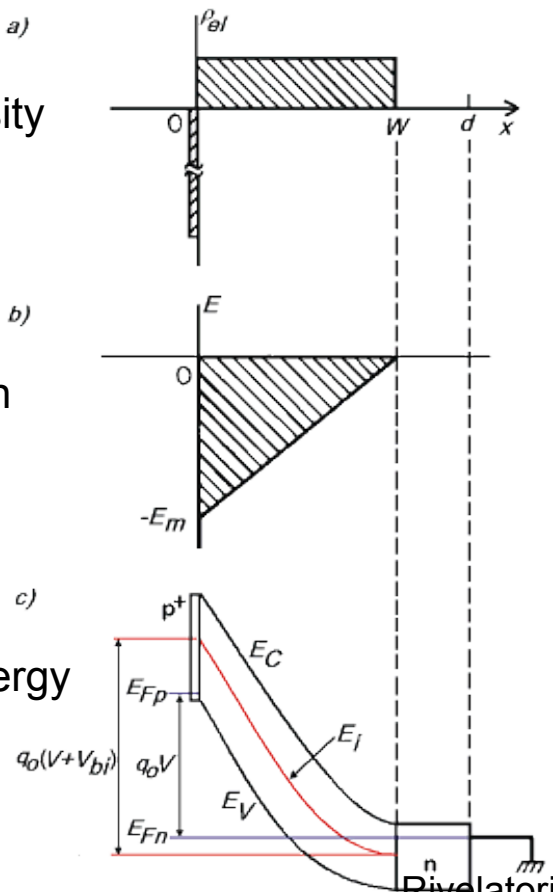


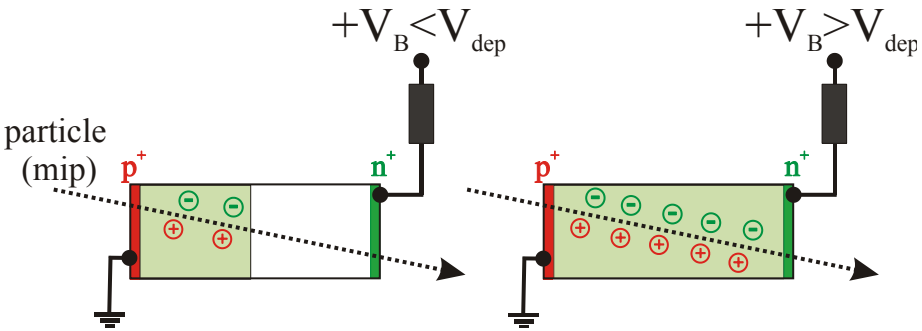
neutral bulk
(no electric field)

Electrical charge density

Electrical field strength

Electron potential energy

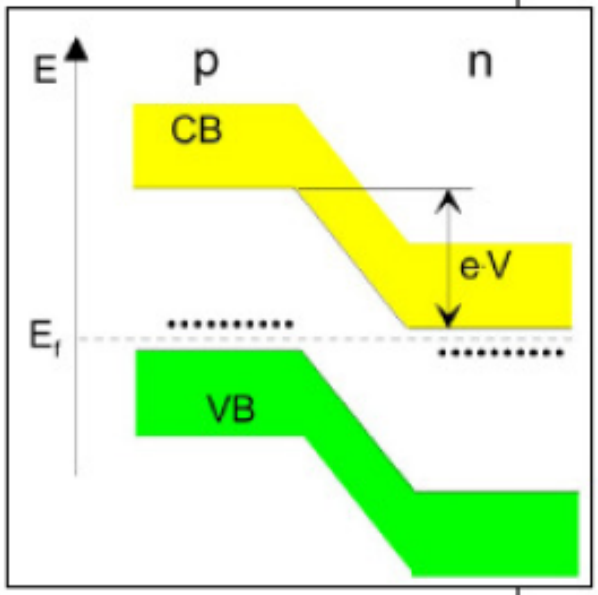


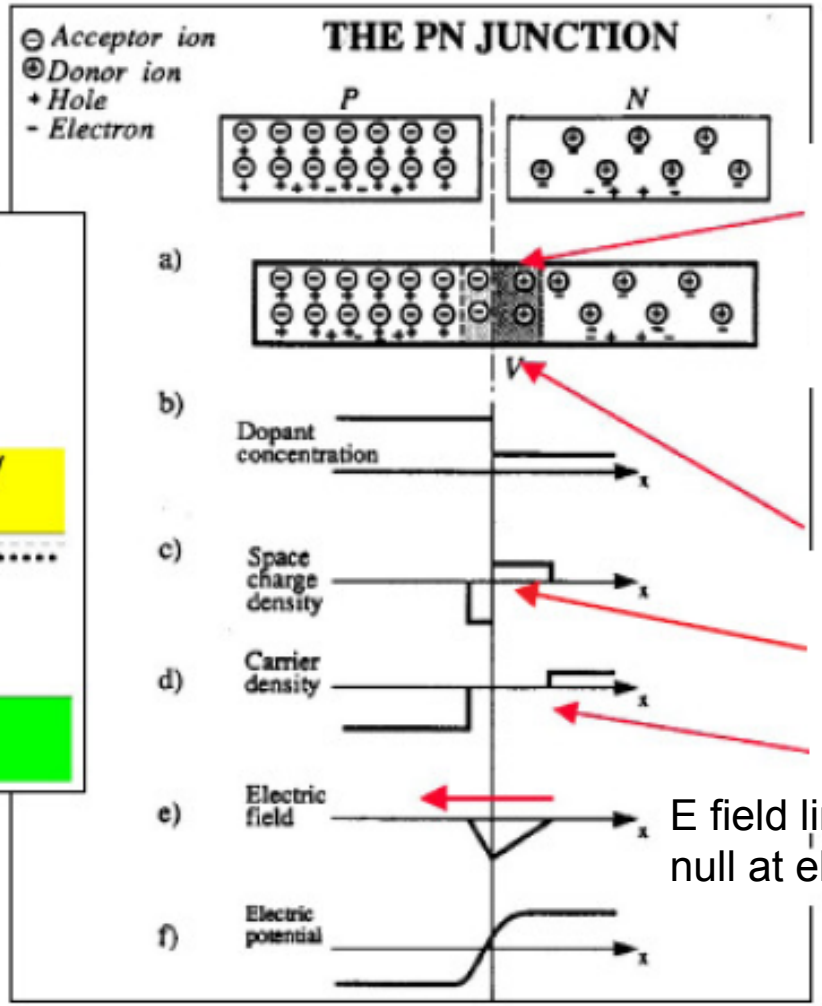


The charge liberated in the undepleted region is not collected since there is no electric field applied in that region and the free charge is much greater.

Full charge collection only for $V_B > V_{dep}$!

P-N junction: overview





Diffusion of e in p zone

Diffusion of holes in n zone

Depletion zone

Asymmetry due to different levels of doping

No free charge carriers

E field linear, max at junction, null at electrodes

Potential quadratically increasing in the space charge region between 0 at p and +V at n side

Rivelatori a stato solido

Capacità

Siccome abbiamo una carica associata alla giunzione dipendente dal voltaggio possiamo parlare di una capacità di carica spaziale:

$$C_j = \frac{dQ}{dV_B} = \frac{dQ}{dw}\frac{dw}{dV_B}$$

Abbiamo chiamato w la profondità della zona di svuotamento.

L' incremento di carica dQ si ha ai lati della giunzione a causa dell'allargamento di w dw causato dalla crescita dV_B del voltaggio di bias V_B. ➔

$$dQ = qN_d dw$$

$$\frac{dw}{dV_B} = \frac{dx_n}{dV_B} = \sqrt{\frac{q\varepsilon N_a \cdot N_d}{2(N_a + N_d)V_B}}$$

$$\Rightarrow \quad C_j = \sqrt{\frac{\varepsilon}{2\mu\rho V_B}}$$

Capacità per unità di area.

L'ordine di grandezza di C_j è ~150÷200 pF/cm^2 per V_B~100V

Se A è l'area avremo $C_j=(\varepsilon/w)A$

Rivelatori a stato solido

- In condizioni di reverse bias, la corrente NON e' zero.
- Due contributi: correnti di generazione e correnti di diffusione

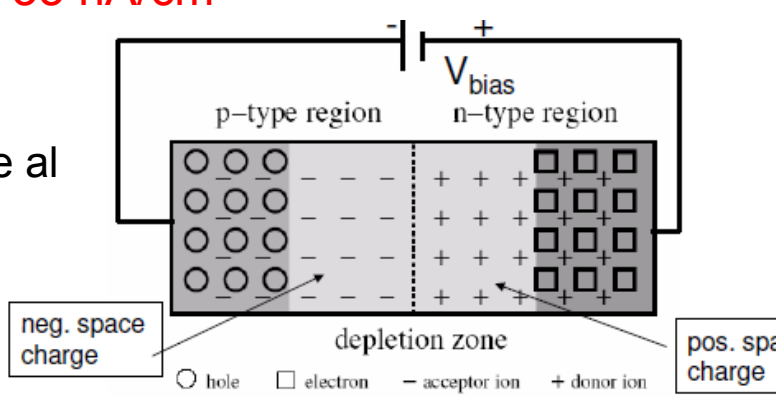
$j_{diff}=q(n_p/\tau_n)L_n + q(p_n/\tau_p)L_p$ (L = lunghezza di diffusione). Queste correnti di diffusione sono dovute alle cariche generate vicino alla zona di svuotamento e che diffondono nella zona di svuotamento stessa. Se τ_p e τ_n sono le vite medie dei p nella regione n e degli n nella regione p le lunghezze di diffusione saranno $L_n=(D_n\tau_n)^{1/2}$ e $L_p=(D_p\tau_p)^{1/2}$, essendo D la costante di diffusione.

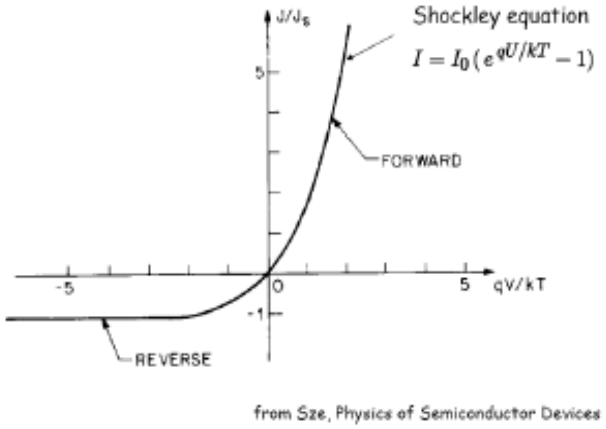
$j_{gen}=(1/2)(n_i/\tau_o)d$ corrente dovuta alla carica generata nella zona di svuotamento. τ_o= vita media dei portatori minoritari nella zona di svuotamento: dipende da difetti e impurita' del reticolo che ricombinano/intrappolano la carica. Questa corrente è proporzionale a **d**, lo spessore svuotato, che a sua volta è proporzionale a $(V_B)^{1/2}$. n_i dipende fortemente dalla temperatura (raddoppia ogni 8°) ➔ mantenere il silicio a temperatura costante.

Il contributo delle due correnti dipende dal materiale: nel Ge domina la corrente di diffusione, mentre nel Si domina la corrente di generazione.

Valori tipici nel Si, 35 nA/cm^2

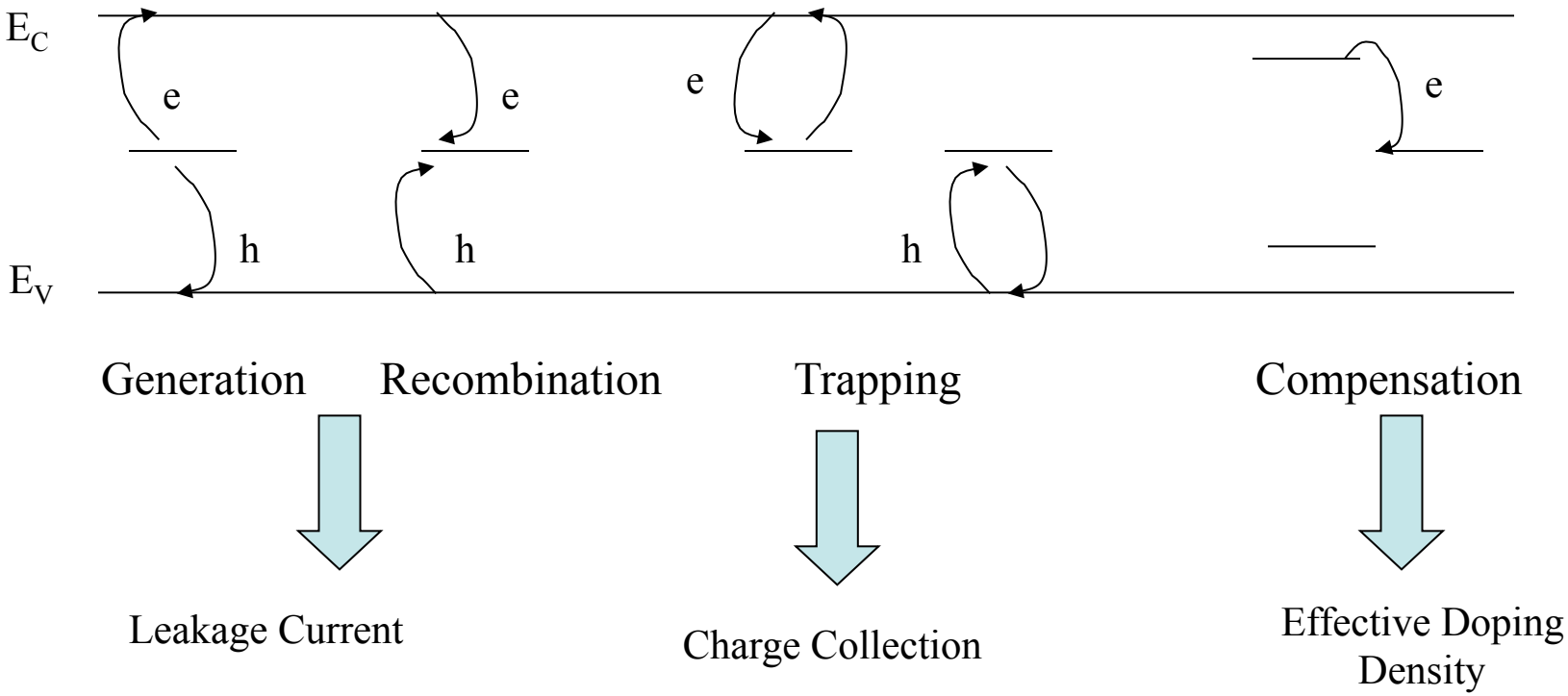
La corrente del rivelatore contribuisce al rumore dell'apparato





Effects of Defects

I difetti creano livelli di energia nella gap, che oltre a generare corrente di leakage, possono intrapolare elettroni e lacune per un tempo lungo rispetto a quello di raccolta oppure possono ricombinare coppie catturate dalla stessa trappola → **inefficienze di raccolta di carica.**
Fisicamente sono creati da impurita' per sostituzione, interstiziale, dislocamento di piani reticolari, cioe' da tutti quei fattori che "distorcono" il potenziale periodico "ideale" del reticolo del semiconduttore...p. es irraggiamento



La statistica di questi processi e' descritta da quella di Shockley Read Hall (che non facciamo)

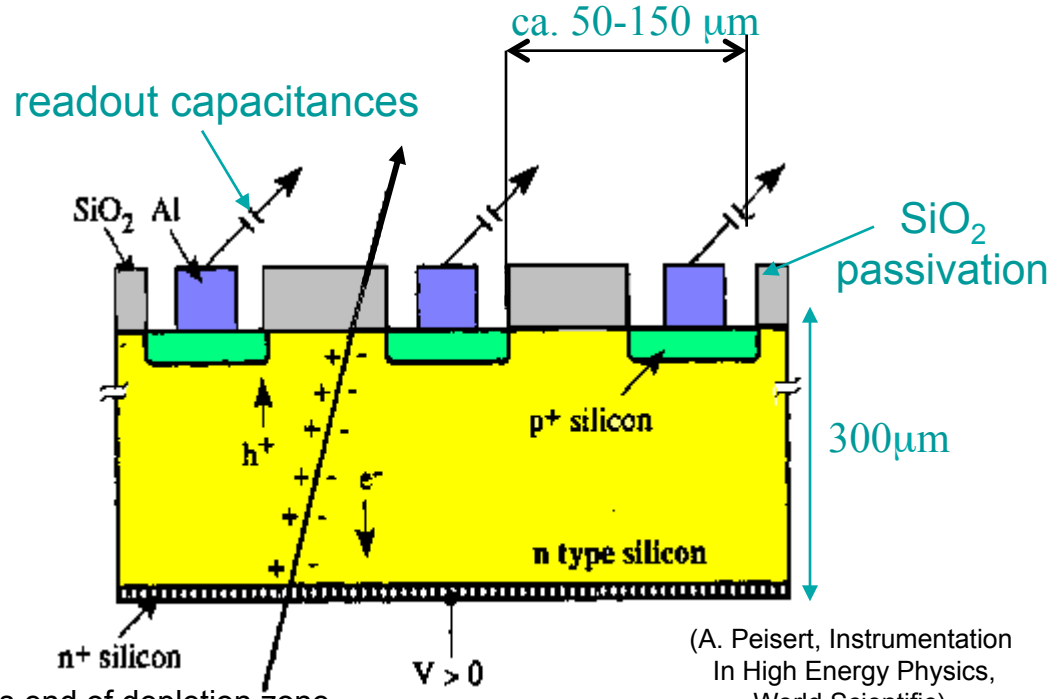
Rivelatori a stato solido

- Polarizzando inversamente il diodo (cioè applicando una V_B~100V dello stesso segno di V_d) → la sottile zona di carica spaziale si estende su tutto il diodo → diodo completamente svuotato. $d \sim x_n \sim (2\varepsilon V_B/qN_d)^{1/2}$ ed in termini della resistività ρ $d=(2\varepsilon V_B \rho \mu)^{1/2}$
- Il deposito di energia nella zona completamente svuotata, dovuto al passaggio della particella carica, crea delle coppie libere e-lacuna.
- Sotto l'influenza del campo elettrico, gli elettroni derivano verso il lato n, le lacune verso il lato p → si ha una corrente rivelabile
- la parte p serve :
  - per poter svuotare la parte n e quindi può essere molto sottile
  - per raccogliere le lacune che si sono formate

Rivelatori a stato solido

Possiamo usare la zona di carica spaziale svuotato come camera di ionizzazione. **Segmentando una o entrambe le faccie possiamo misurare la coordinata del punto di passaggio della particella ionizzante.**

Larghezza tipica microstrip 15 um
Passo tipico 50-100 um (da confrontare con quello delle camere a deriva e TPC, 1-2 mm)



defines end of depletion zone
+ good ohmic contact

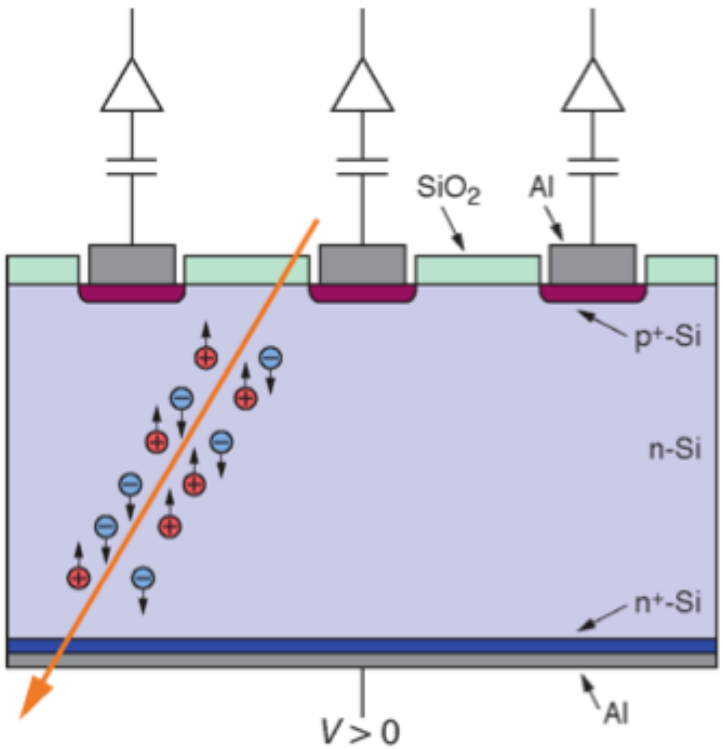
(A. Peisert, Instrumentation In High Energy Physics, World Scientific)

Rivelatori a stato solido

Through going charged particles create e^-h^+ pairs in the depletion zone (about 30.000 pairs in standard detector thickness). These charges drift to the electrodes. The drift (current) creates the signal which is amplified by an amplifier connected to each strip. From the signals on the individual strips the position of the through going particle is deduced.

A typical n-type Si strip detector:

- ★ p^+n junction:
  $N_a \approx 10^{15}$ cm^{-3}, $N_d \approx 1–5 \cdot 10^{12}$ cm^{-3}
- ★ n-type bulk: $\rho > 2$ kΩcm
  ➔ thickness 300 μm
- ★ Operating voltage < 200 V.
- ★ n^+ layer on backplane to improve ohmic contact
- ★ Aluminum metallization



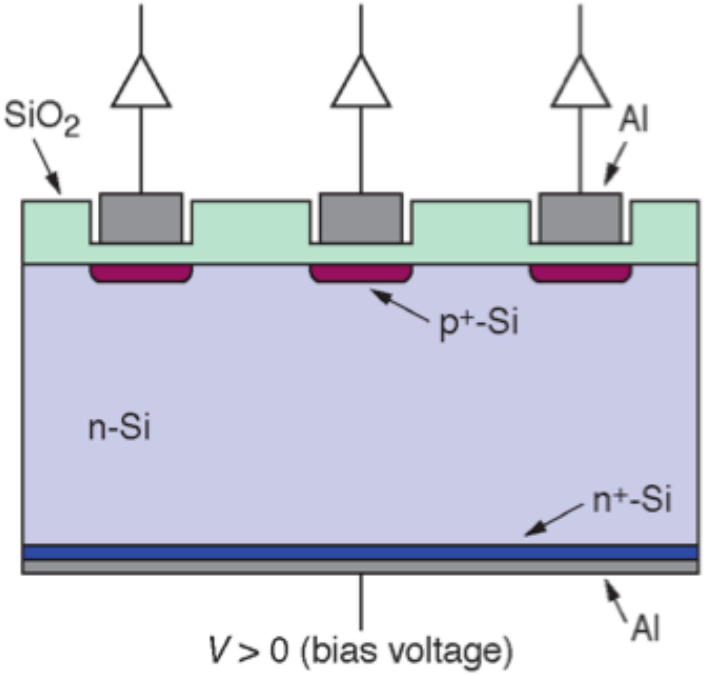
AC Coupling

La corrente delle strip va disaccoppiata dall'ingresso del circuito di lettura poiche' saturerebbe l'amplificatore.
Si inserisce un condensatore fra la strip e il circuito esterno, Puo' essere discreto o integrato

AC coupling blocks leakage current from the amplifier.

- ★ Integration of coupling capacitances in standard planar process.
- ★ Deposition of SiO_2 with a thickness of 100–200 nm between p+ and aluminum strip
- ★ Depending on oxide thickness and strip width the capacitances are in the range of 8–32 pF/cm.
- ★ Problems are shorts through the dielectric (pinholes). Usually avoided by a second layer of Si_3N_4.

AC coupled strip detector:



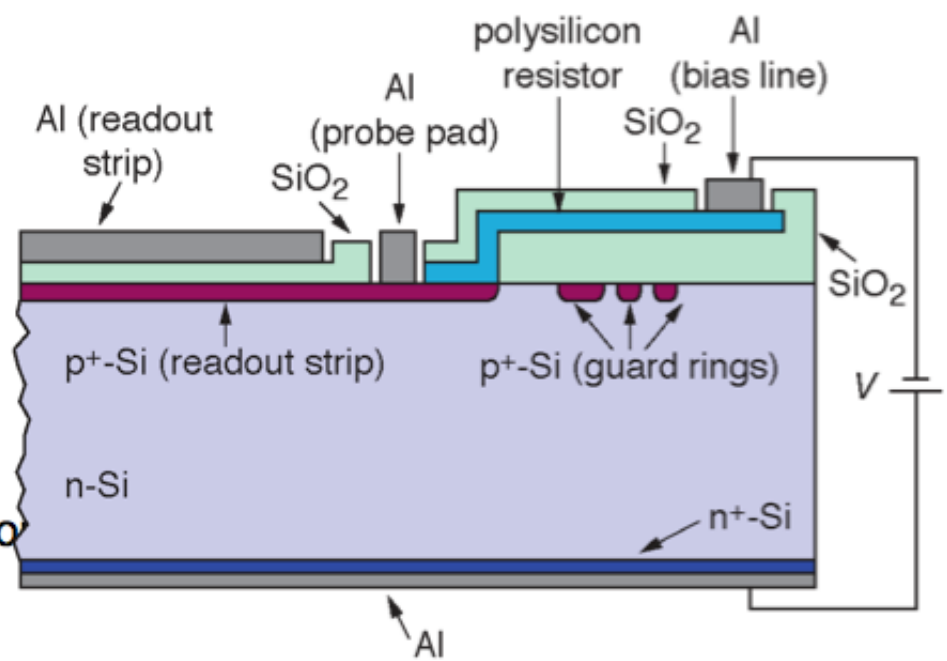
Several methods to connect the bias voltage: polysilicon resistor, punch through bias, FOXFET bias.

Alimentazione del detector (bias)

- Ciascuna strip va tenuta a tensione V (che puo' essere massa, per esempio sulla faccia di giunzione)
- La densita' di strip e' molto alta e le strip sono molto piccole (10 um x qualche cm)
- Occorre una "linea" di alimentazione per ciascuna strip
- Non e' pensabile usare un "filo" per ogni strip
- Soluzione: linea di alimentazione (o bias) integrata nel rivelatore

Polisilicon Bias

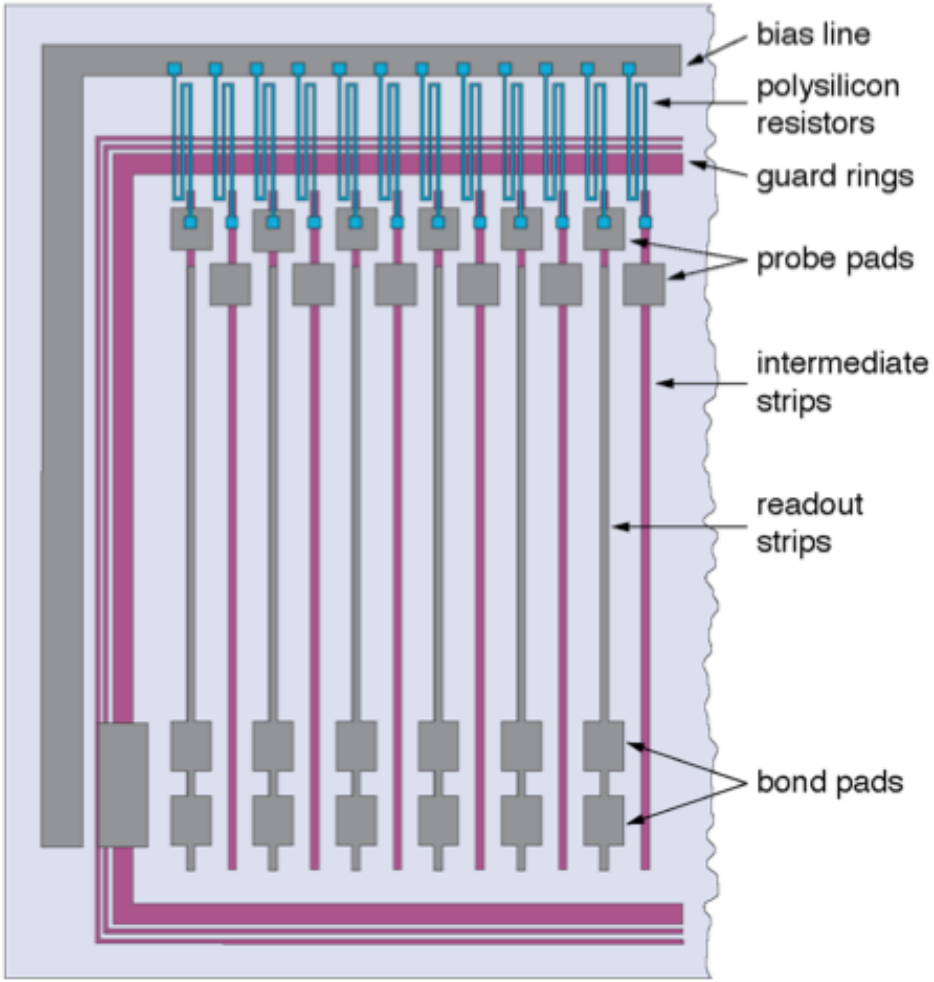
- ★ Deposition of polycristalline silicon between p^+ implants and a common bias line.
- ★ Sheet resistance of up to $R_s \approx 250\ k\Omega/\square$. Depending on width and length a resistor of up to $R \approx 20\ M\Omega$ is achieved ($R = R_s \cdot length/width$).
- ★ To achieve high resistor values winding poly structures are deposited.
- ★ Drawback: Additional production steps and photo lithograpic masks required.



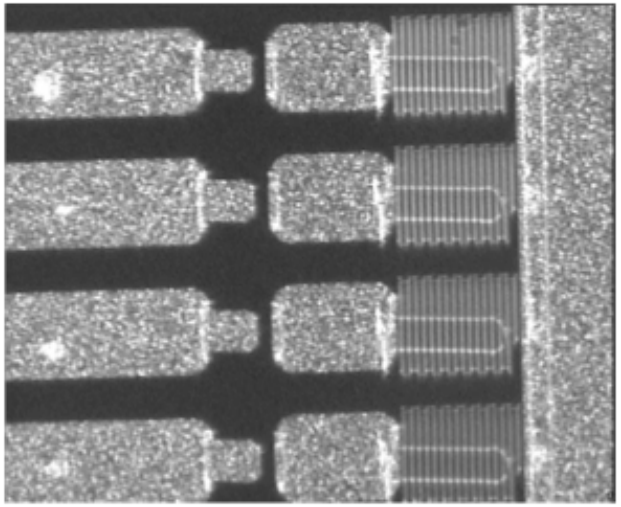
Cut through an AC coupled strip detector with integrated poly resisto

Polisilicon bias

Top view of a strip detector with polysilicon resistors:



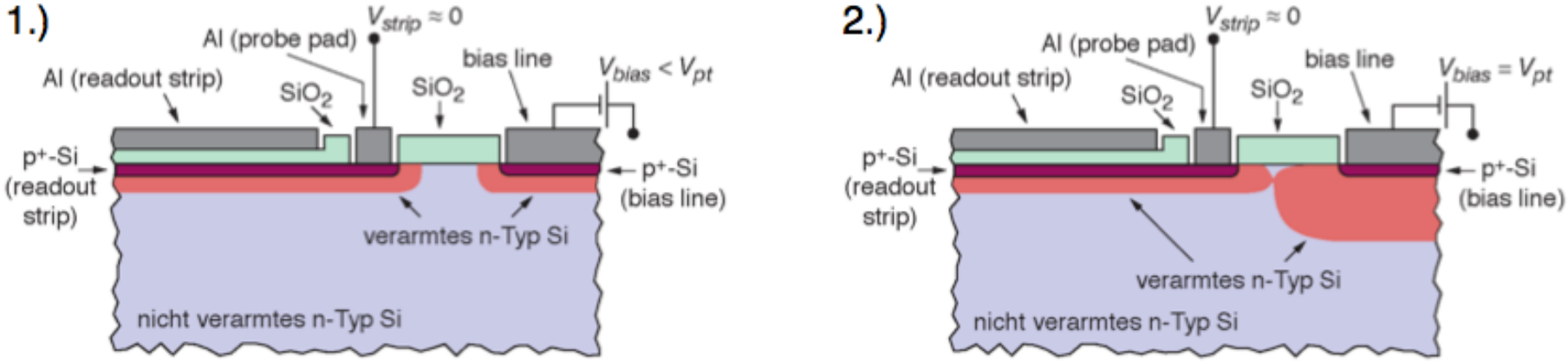
CMS-Microstrip-Detektor: Close view of area with polysilicon resistors, probe pads, stip ends.

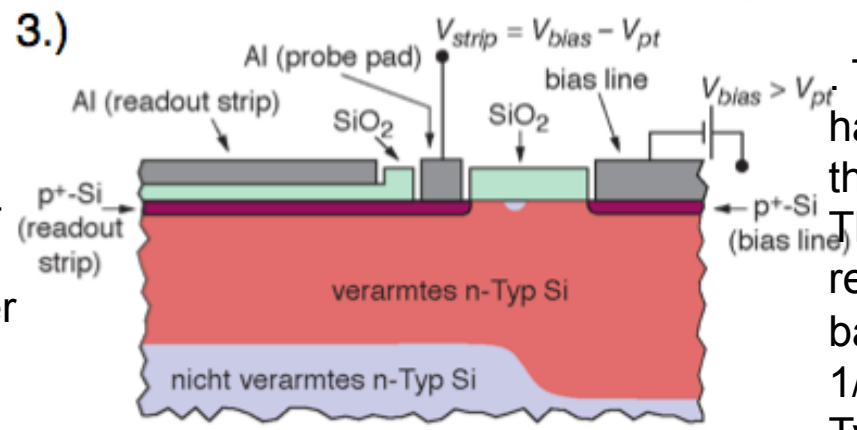


CMS Collaboration, HEPHY Vienna

Punch through bias

Punch through effect: Figures show the increase of the depletion zone with increasing bias voltage (V_{pt} = punch through voltage).





The effect is due to a net flux of holes from strips to the bias line (called "guard-ring") through a potential barrier. The height of barrier decreases with Vbias.
The current is $I_{pt} = I_s e^{\alpha V_{pt}}$

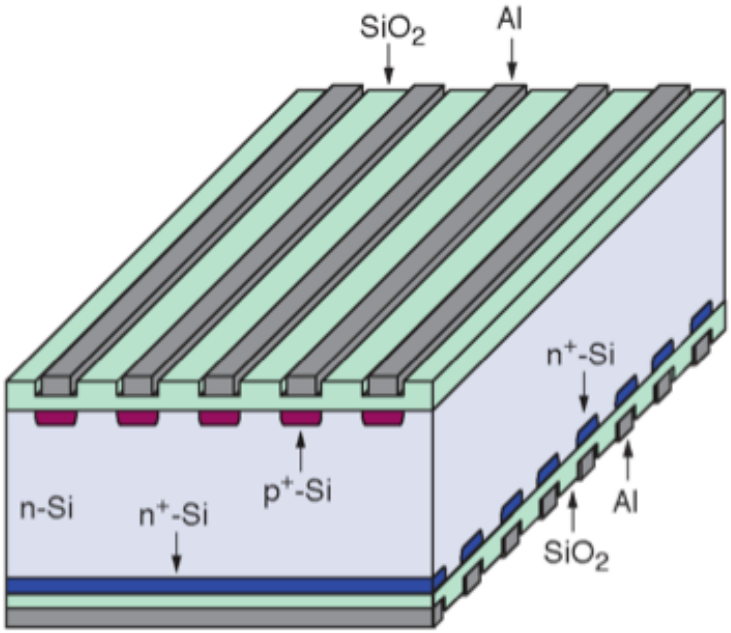
. The result is that the strips have a positive potential wrt the bias line.
The punchthrough resistance depends on the barrier height and is fixed by $1/R_{pt} = dI_{pt}/dV_{pt}$
Typical values of R_{pt} are of O(1-10 GOhm)

Advantage: No additional production steps required.

silici doppia faccia

- ★ Single sided strip detector measures only one coordinate. To measure second coordinate requires second detector layer.
- ★ Double sided strip detector measures two coordinates in one detector layer (minimizes material).
- ★ In n-type detector the n^+ backside becomes segmented, e.g. strips orthogonal to p^+ strips.
- ★ Drawback: Production, handling, tests are more complicated and hence double sided detector are expensive.

Scheme of a double sided strip detector (biasing structures not shown):



Silici doppia faccia

★ Problem with n^+ segmentation: Static, positive oxide charges in the Si-SiO_2 interface.

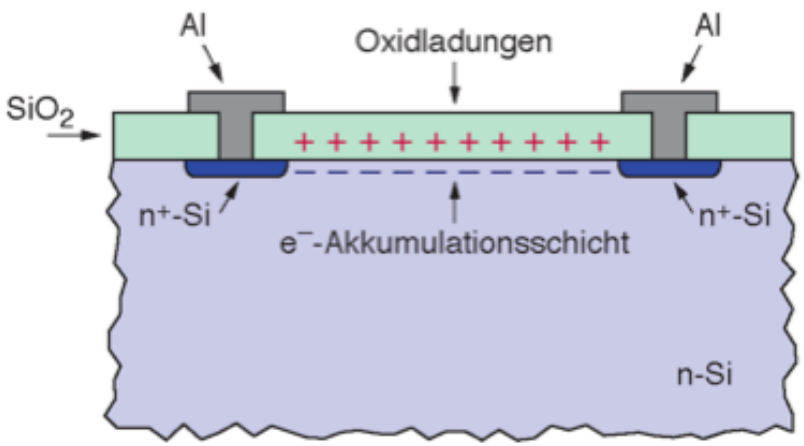
➔ These positive charges attract electrons. The electrons form an accumulation layer underneath the oxide.

➔ n+ strips are no longer isolated from each other (resistance ≈ kΩ).

➔ Charges generated by through going particle spread over many strips.

➔ No position measurement possible.

★ Solution: Interrupt accumulation layer using p^+-stops, p^+-spray or field plates.

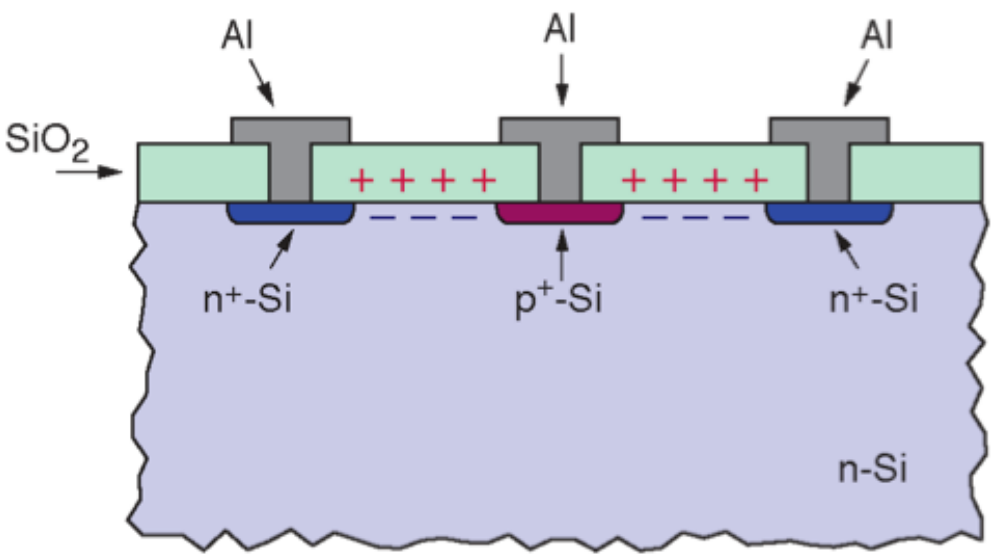


Positive oxide charges cause electron accumulation layer.

Silici doppia faccia

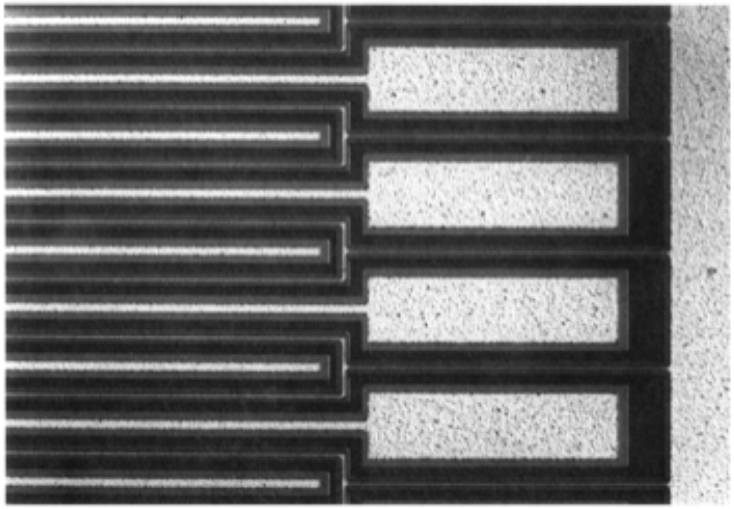
★ p^+-implants (p^+-stops, blocking electrodes) between n^+-strips interrupt the electron accumulation layer.

➔ Interstrip resistance reach again GΩ.



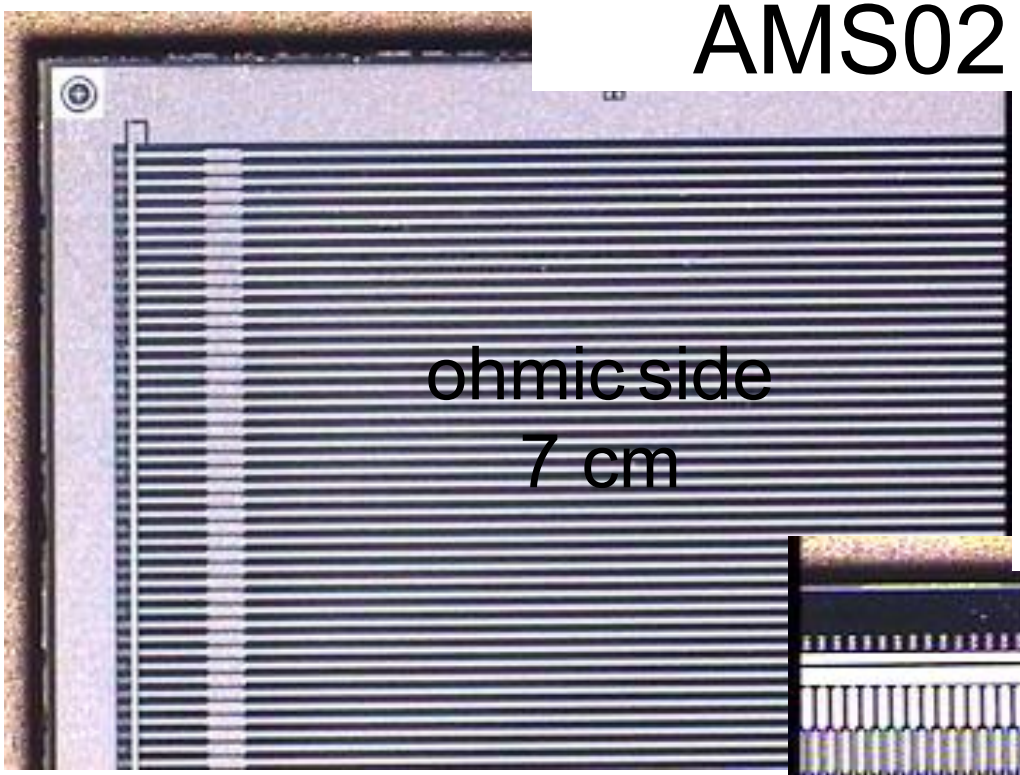
A. Peisert, *Silicon Microstrip Detectors*, DELPHI 92-143 MVX 2, CERN, 1992

Picture showing the n^+-strips and the p^+-stop structure:

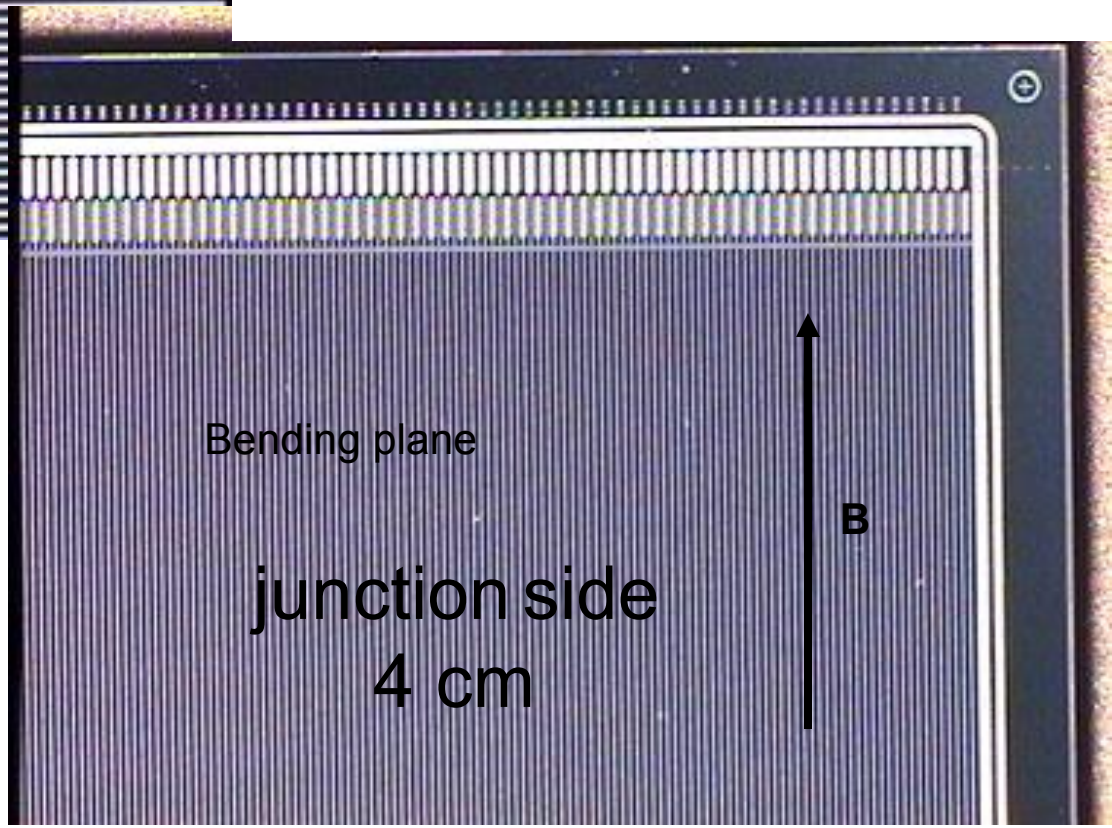


J. Kemmer and G. Lutz, *New Structures for Position Sensitive Semiconductor Detectors*, Nucl. Instr. Meth. A **273**, 588 (1988)

AMS02 silicon detector



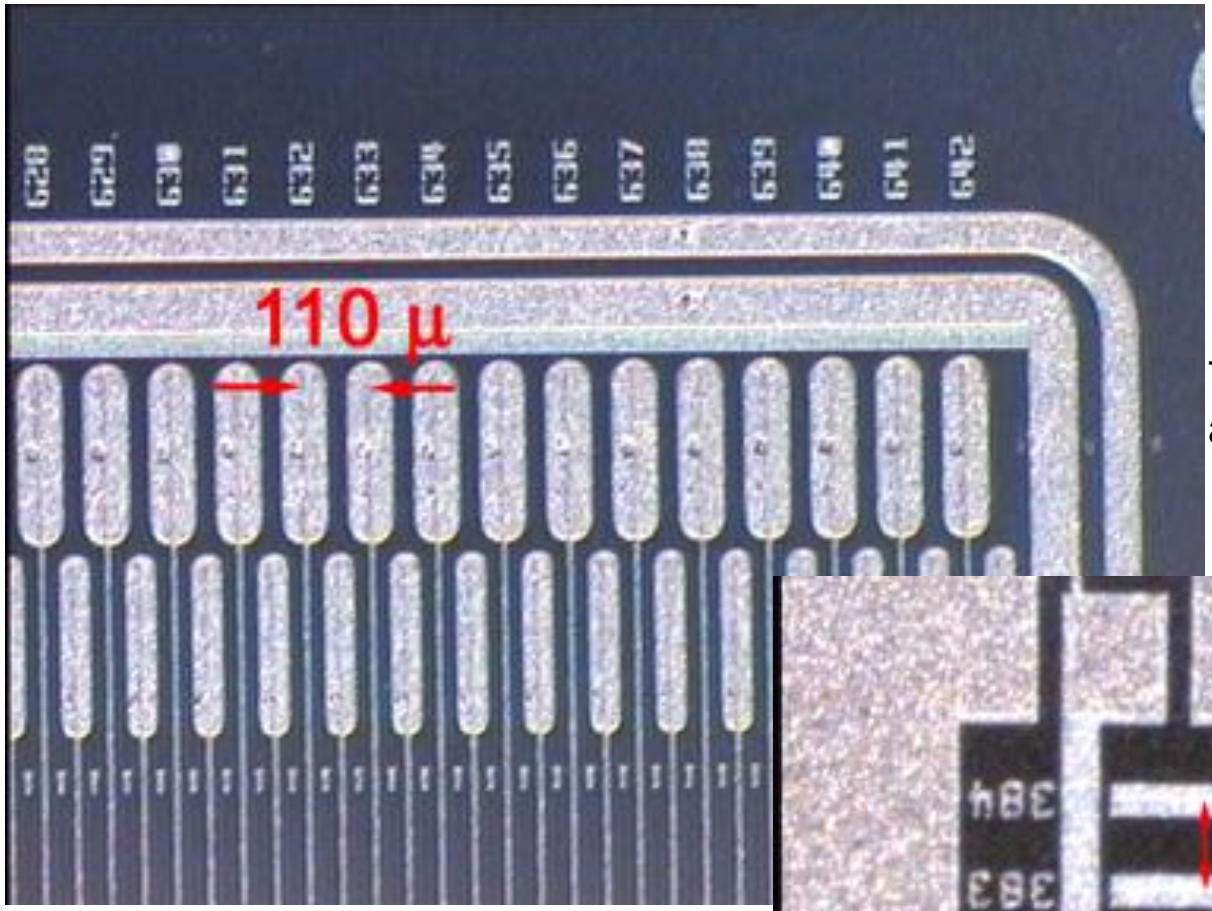
- 1264 p^+ strip junctions on one side (junction side) with geometric pitch 55 um
- 384 n^+ (ohmic side) perpendicular to the p^+ strips with geometric pitch 104 um



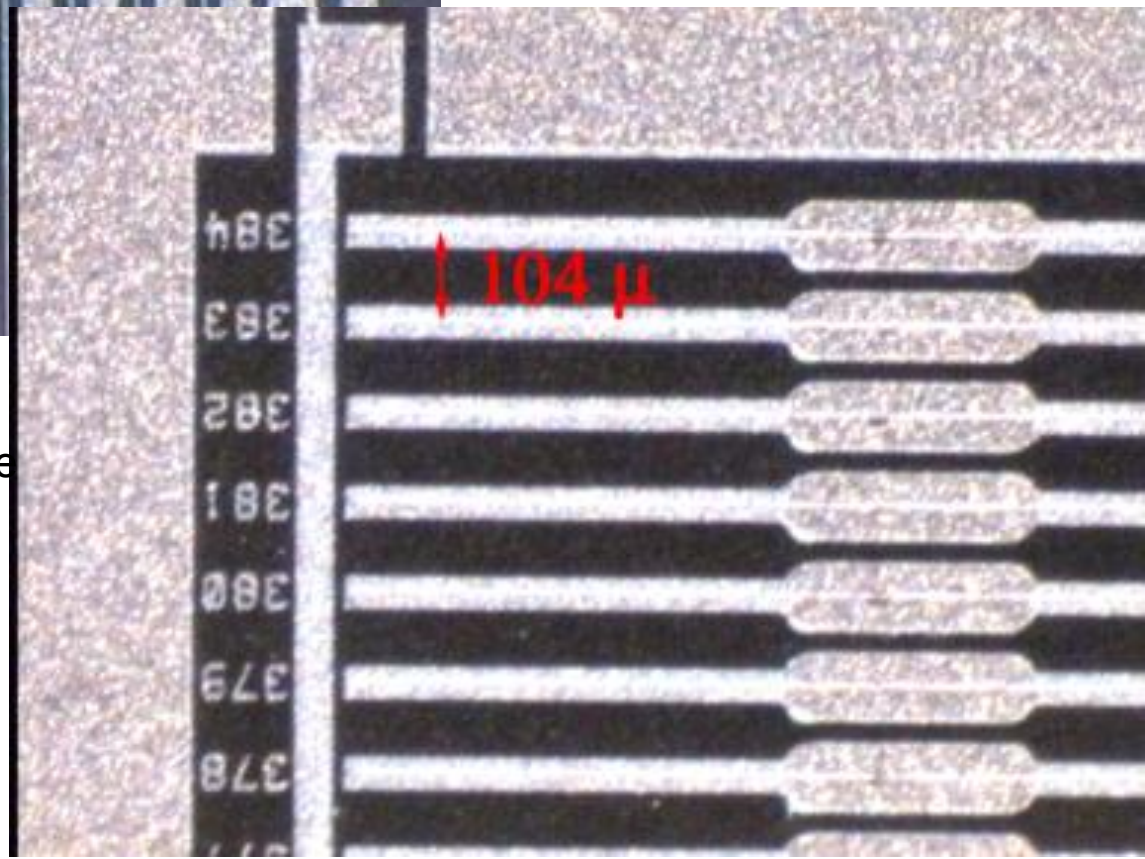
The basic units of the tracker are 300 um-thick, n-doped, high resistivity 7x4 cm^2 silicon sensors. 2500 pieces have been produced for the tracker assembly

I silici del tracciatore di AMS02 sono alimentati con punch through sulla faccia p e con surface through su quella n

The strips are metallized with Al for
with pads for ultrasonic bonding.



The strip width is 12 um on P side
and 40 um on N side



The sensors junctions operate over-
depleted in reverse bias voltage regime
to suppress the leakage current (noise
source).
The operation bias voltage is 75 V

Rivelatori a stato solido

Risoluzione spaziale intrinseca (di singolo punto)

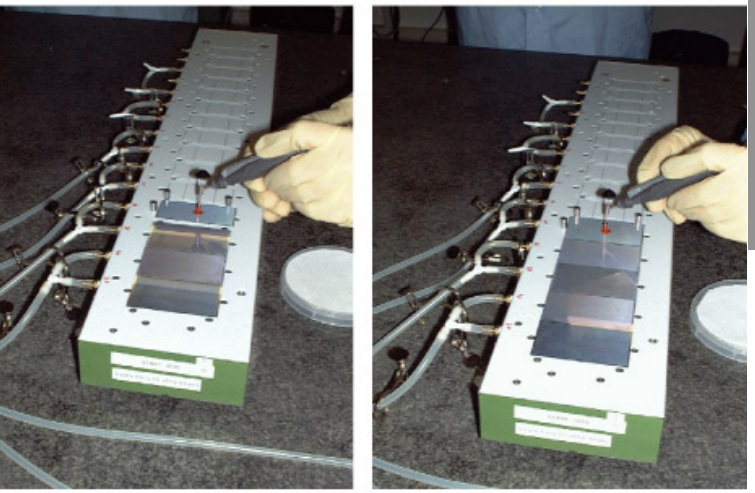
Dipende da:

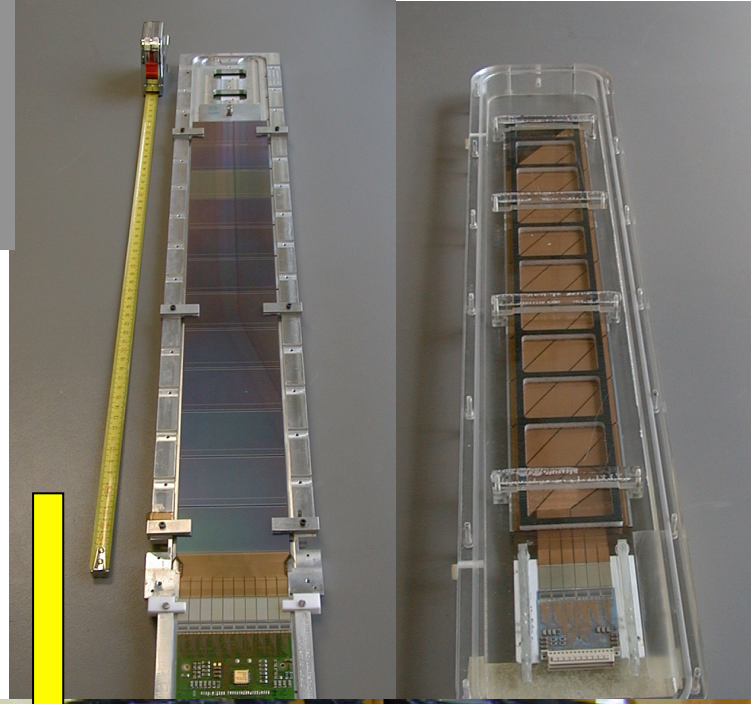
- Processi fisici quali le fluttuazioni della perdita di energia, la diffusione dei portatori di carica.
- Fattori esterni quali il numero di strip, il modo di lettura, il rumore dell'apparato (o meglio S/N)

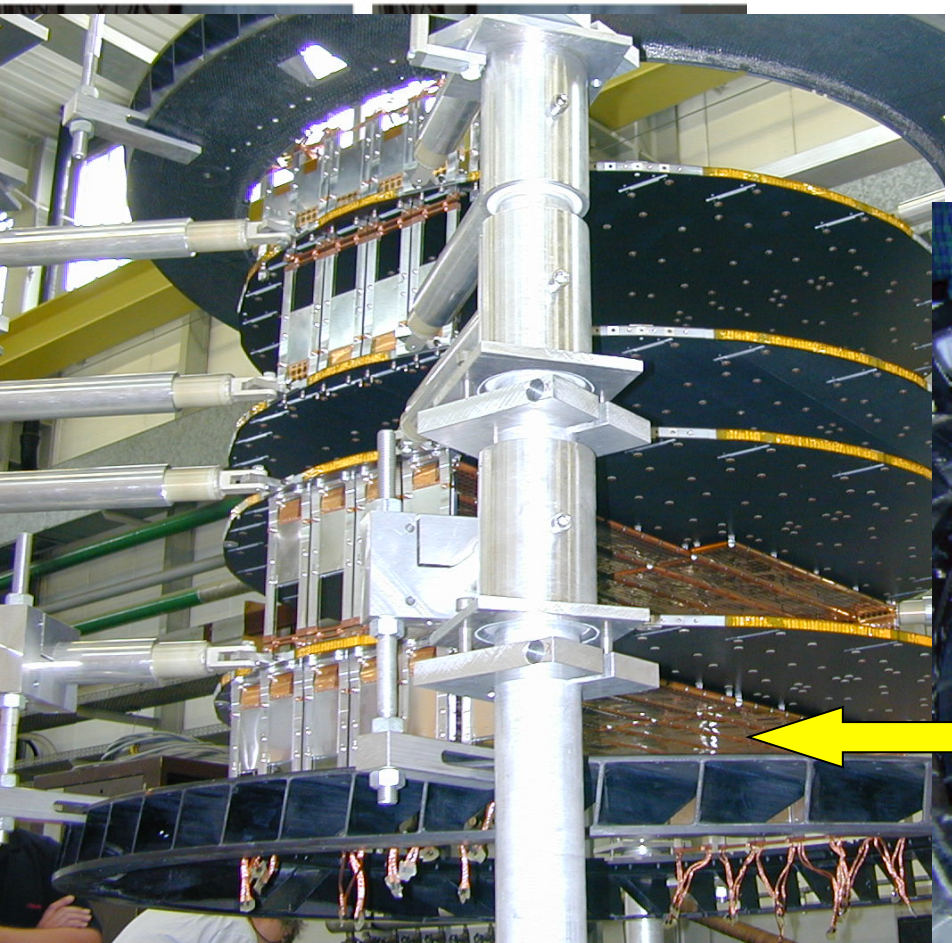
Si possono raggiungere precisioni fino a ~ 3 μm.

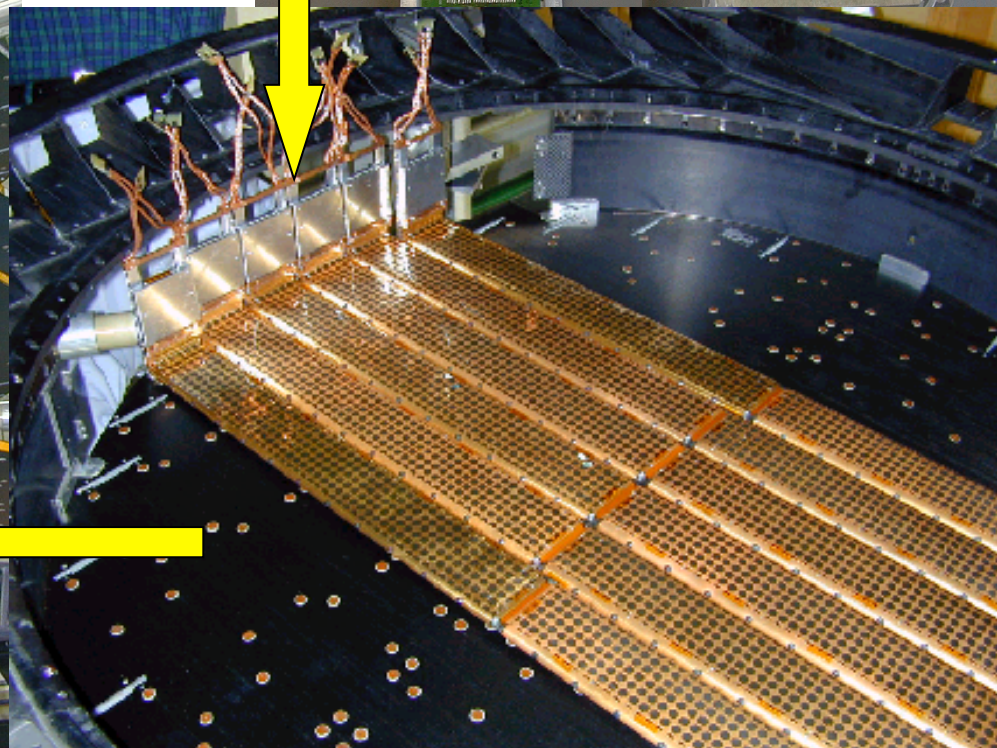
La risoluzione spaziale "totale" dipende in maniera cruciale dall'allineamento relativo dei differenti moduli che costituiscono l'apparato tracciante → necessarie misure continue di posizione relativa dei rivelatori

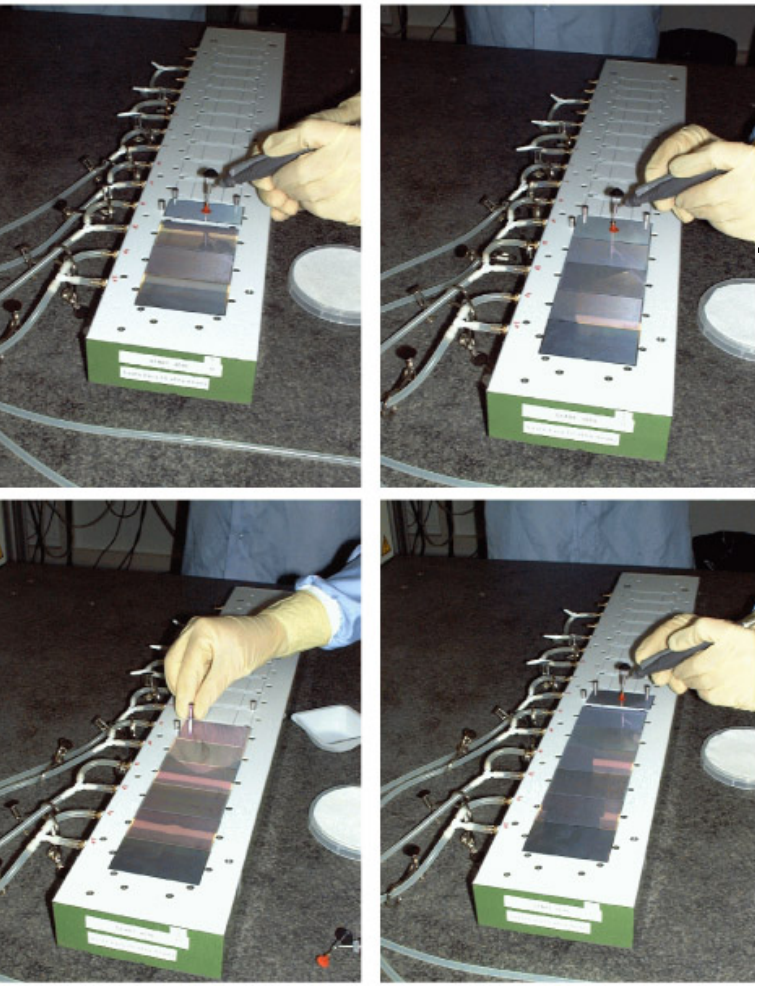
The tracker assembly









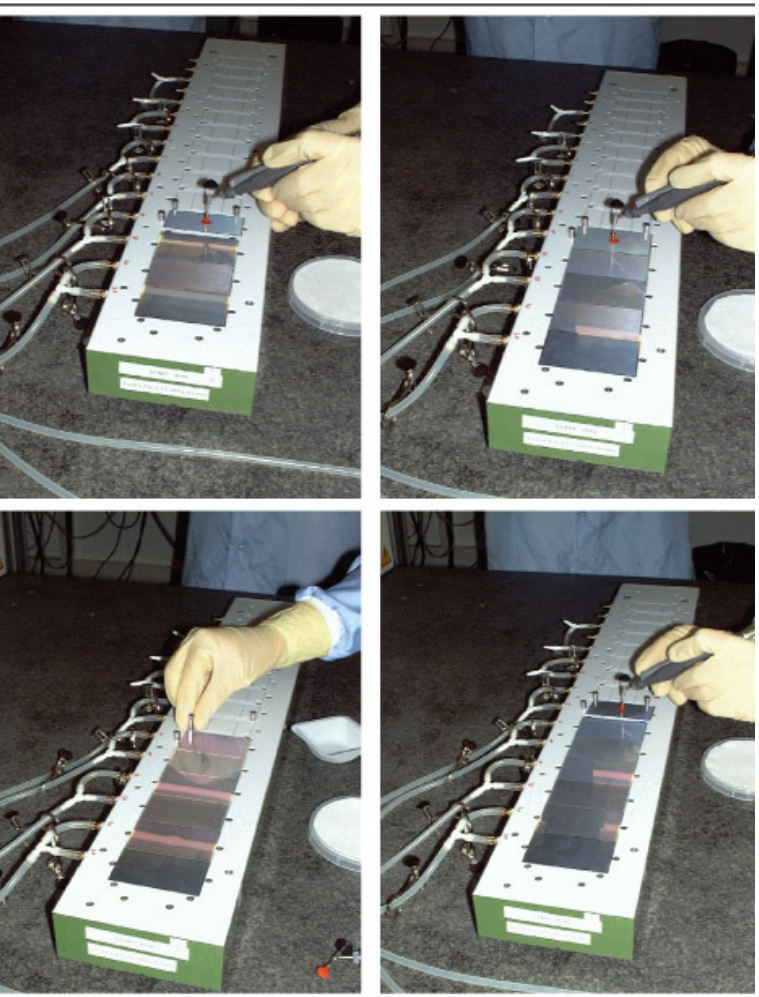


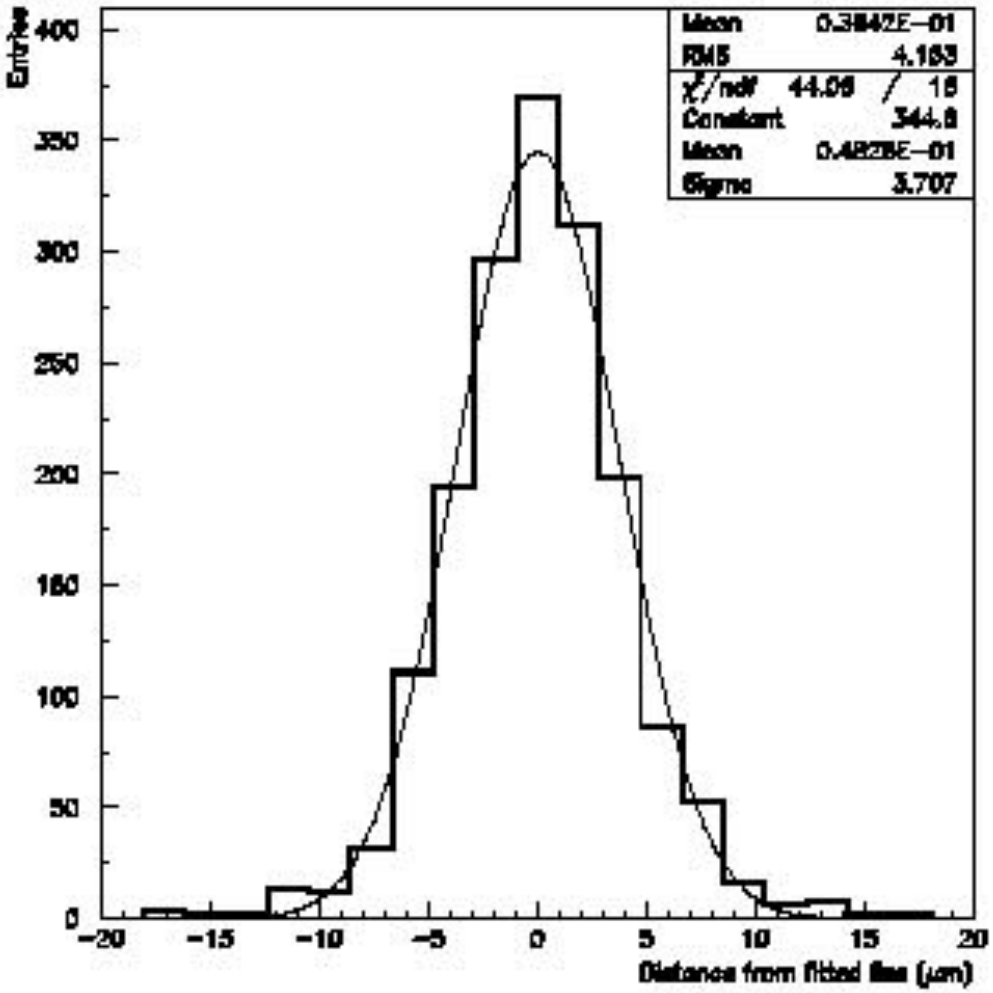
The fundamental unit of the tracker is a ‘ladder’ : a set of mechanically and electrically connected silicon sensors.

To reach the highest position resolution, it is necessary to align the single sensors to a precision of few microns over a lenght up to 60 cm→special high precision jigs have been built to assembly the sensors.

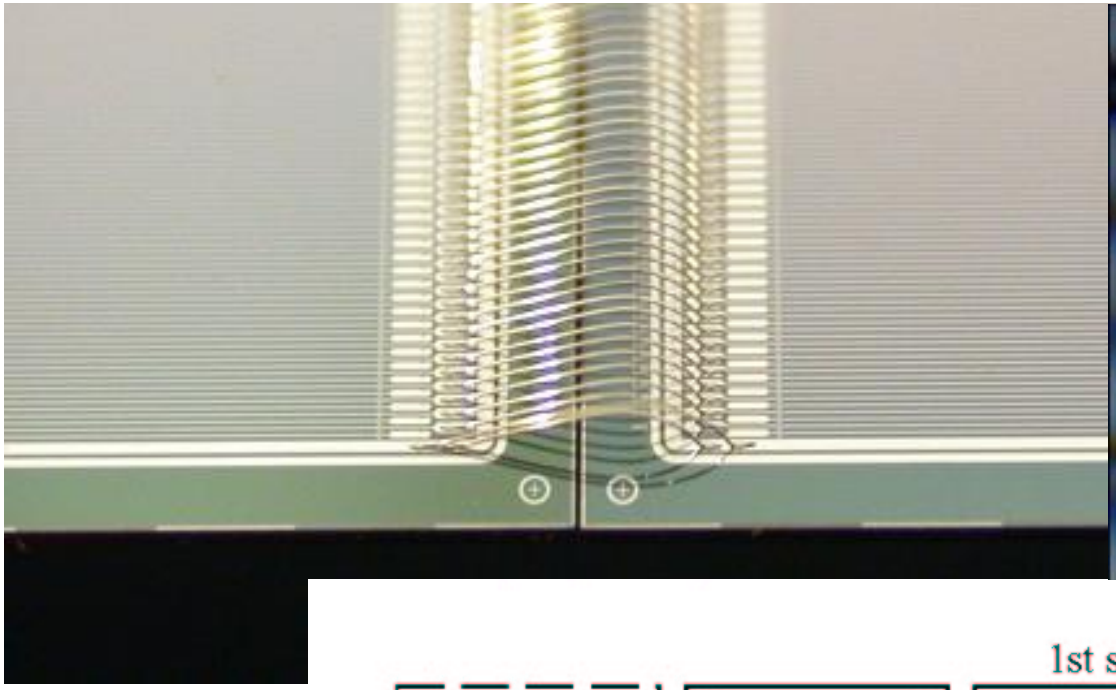
The tolerance on the sensors dimensions is less then 5 um and the distance between 2 adjacent sensors is about 20 um.
The final precision on alignment is better than 4 um

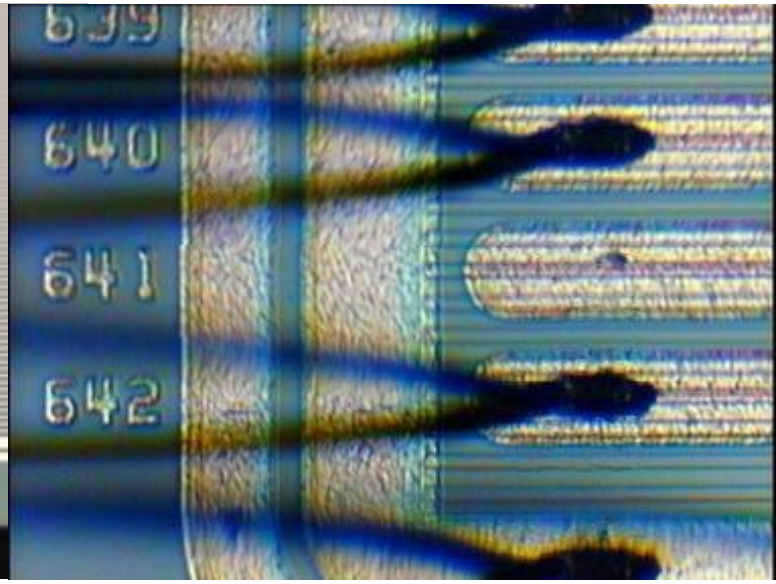
The ladder assembly

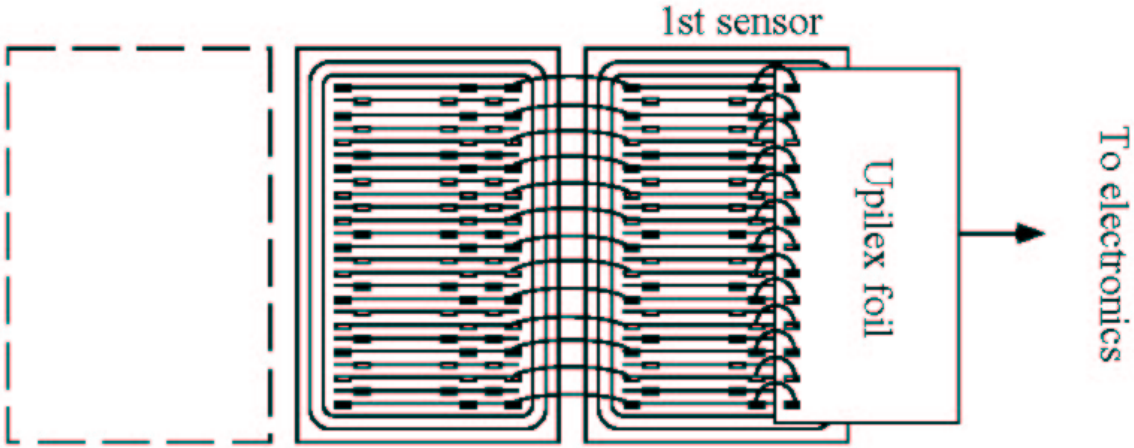




To connect electrically all the sensors in a ladder, all the strips on the P side are daisy-chained with a 25 um Al wire through ultrasonic bondings

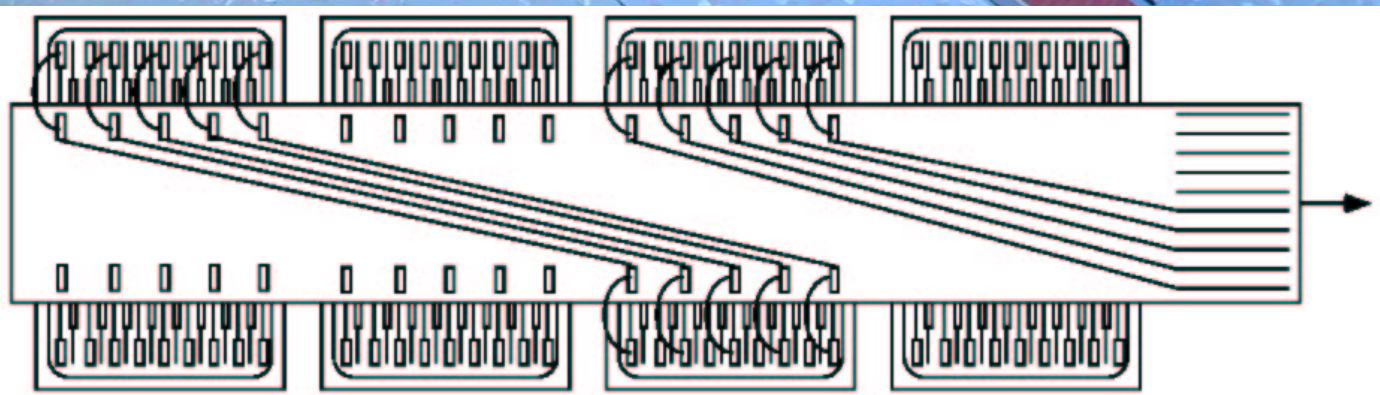


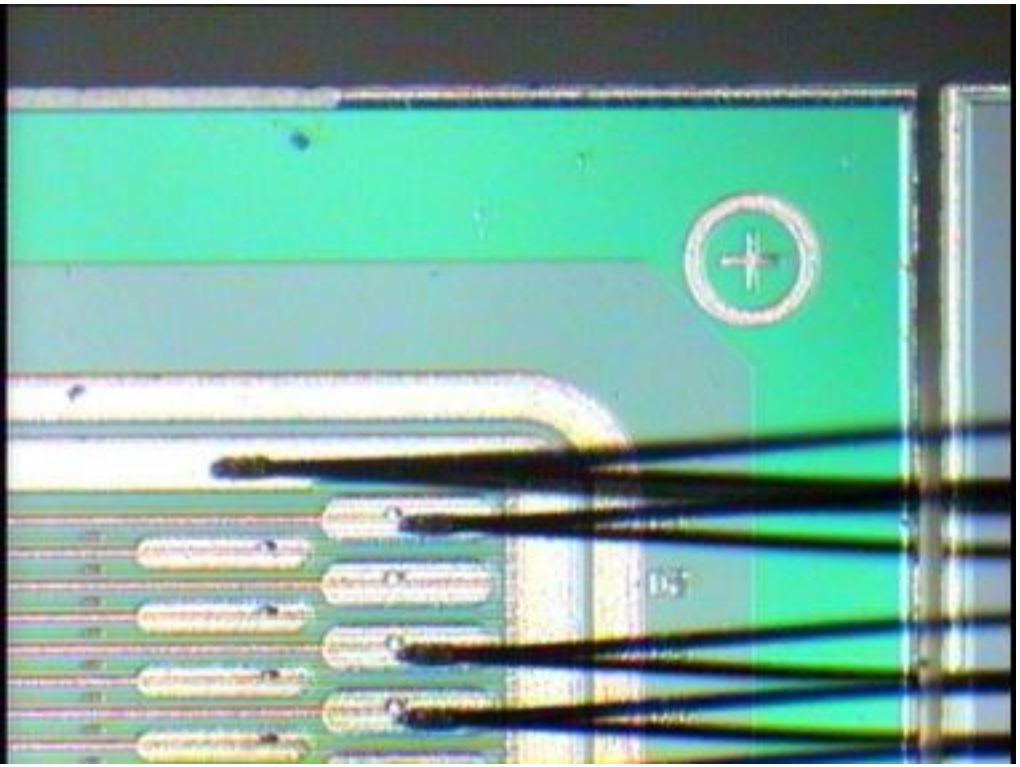




On the N side, the front-end electronics is at one end, so that a special upilex (kapton) fanout to connect the strips to the FEE has been developed

To reduce the number of readout channels, the odd and even strips are connected to same RO channel.
The ambiguity is solved with additional information from TOF





The charge collection is made via the interstrip capacitive charge sharing

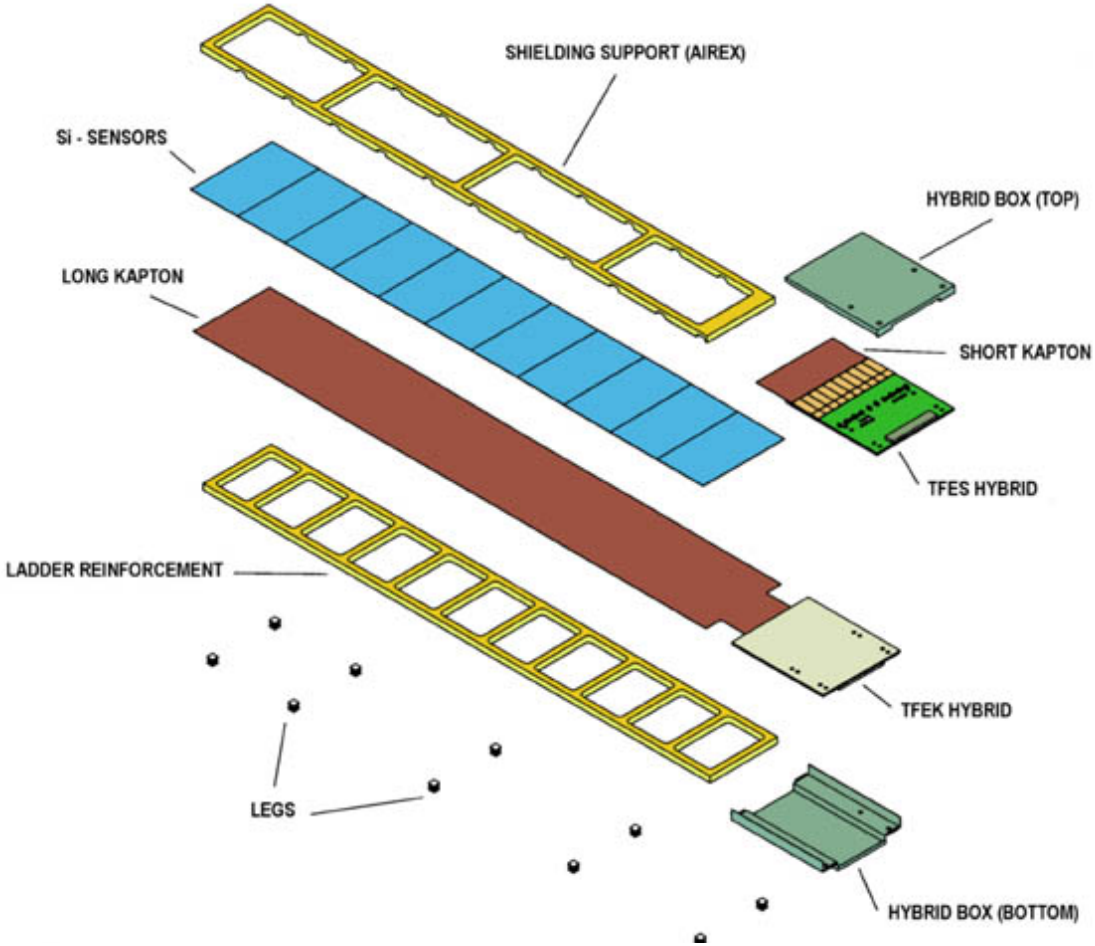
To read out all the strips would result in a too much hugh number of FE electronics channels→ the read out pitch is arranged so that a floating strips is left between two readout strips on both sides→

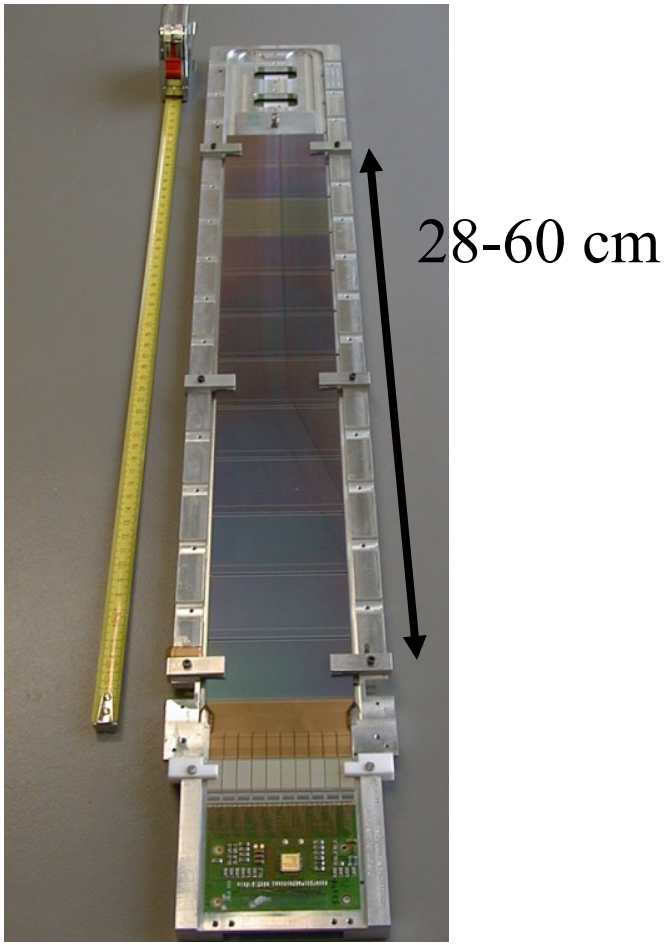
On p side the RO pitch is 110 um → 642 RO chann./ladder

On N side the RO is 208 um→ 384 RO chann./ladder

for 196608 RO channels

Silicon Tracker Ladder



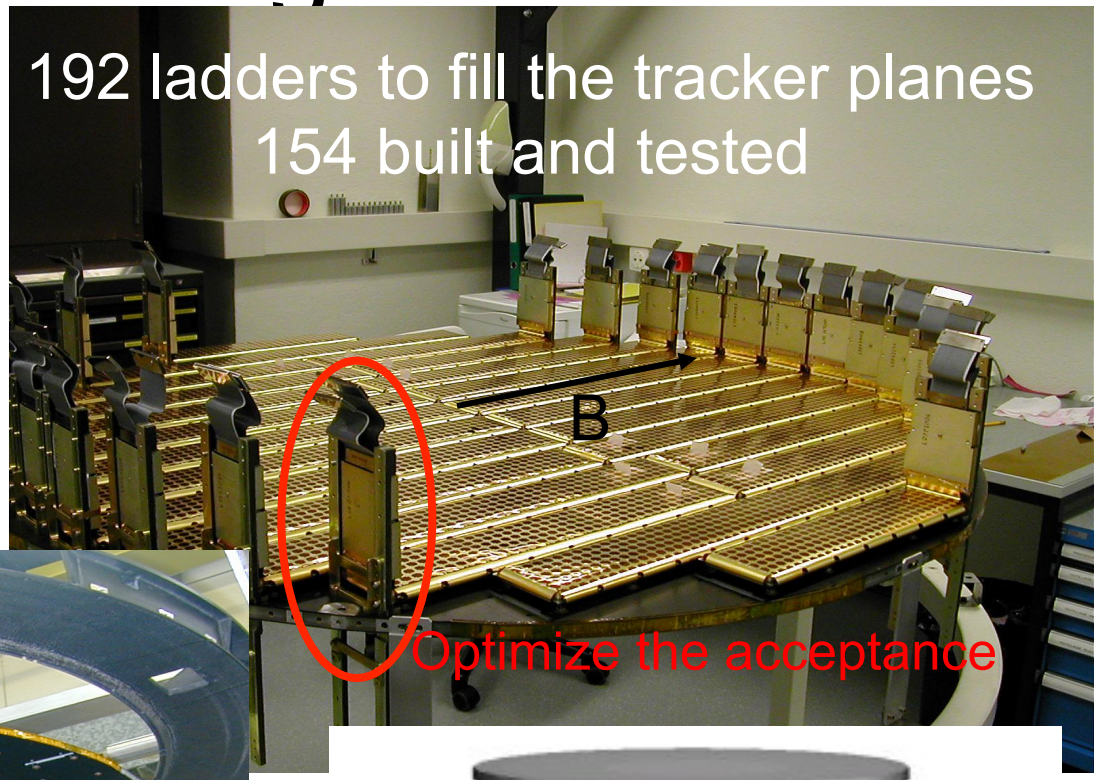


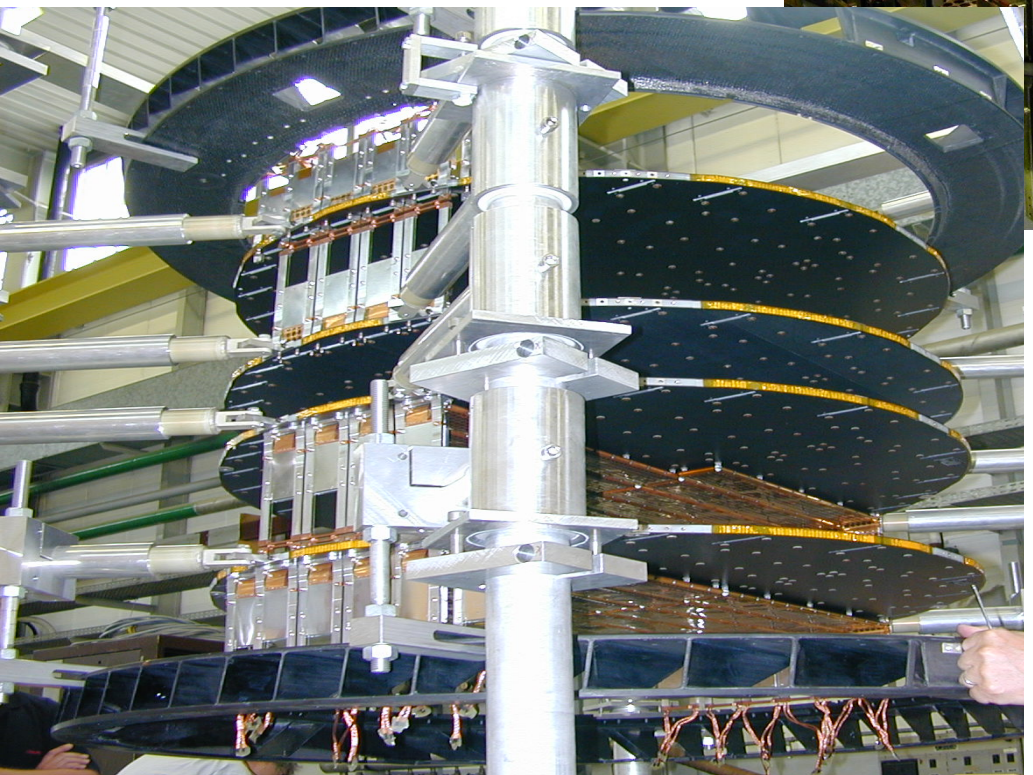
Ladder structural stability is ensured by a carbon fiber foam support structure

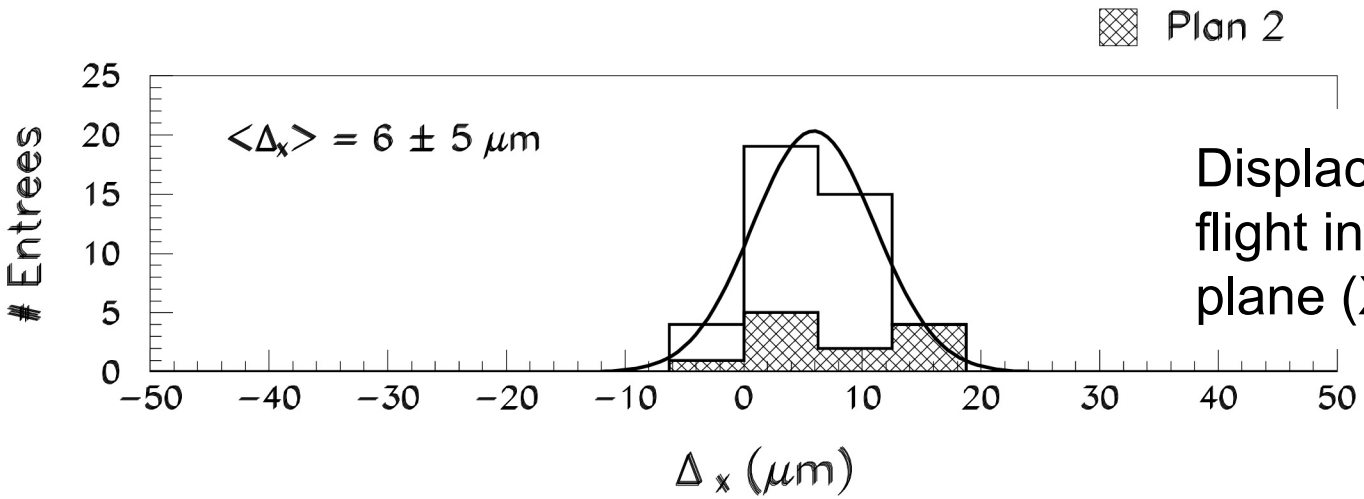
To fit the circular magnet bore, the ladders have lenght from 7 to 15 sensors with 8200- 16500 bonds/ladder

Plane integration

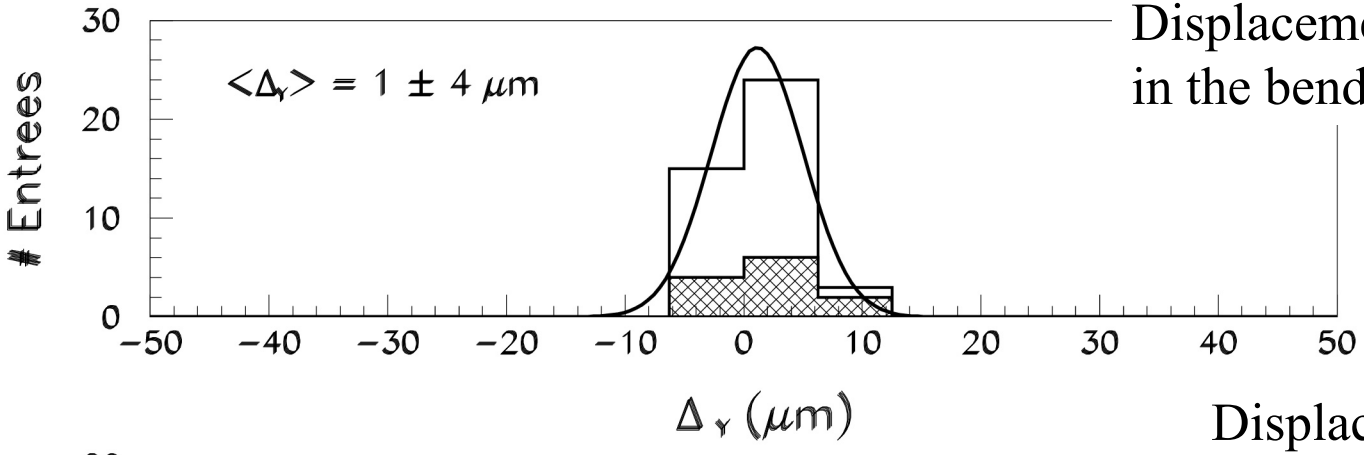
The planes are made a aluminium honeycomb structure enveloped in a carbon fiber foil. This ensures great stability against vibrations and G-forces during the launch



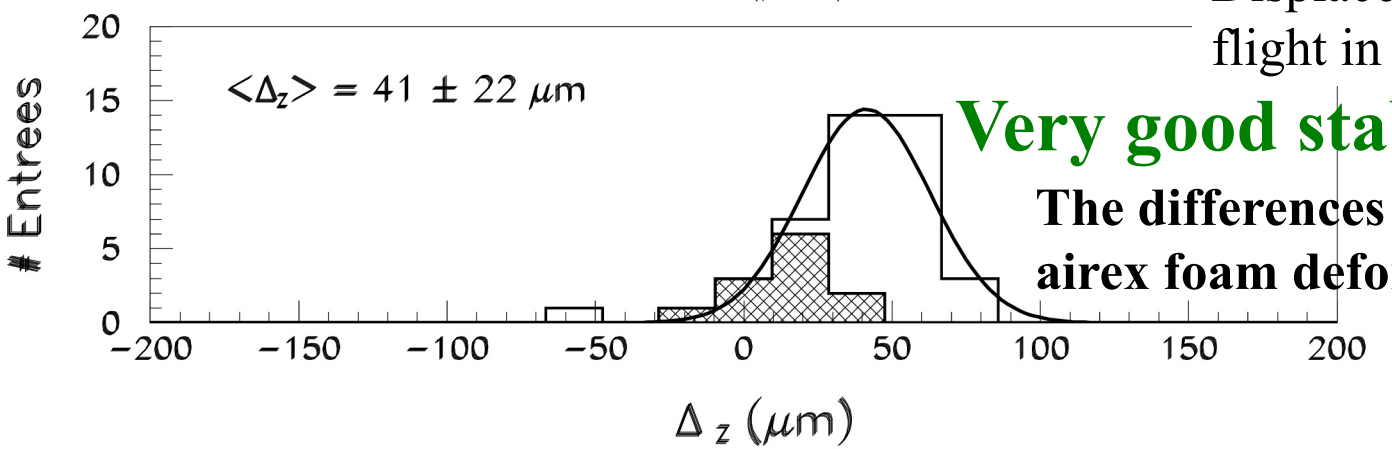




Displacements before/after flight in the non bending plane (X)



Displacements before/after flight in the bending plane (Y)



Displacements before/after flight in the Z

Very good stability of planes

The differences are due to to the airex foam deformation in vacuum

Rivelatori a stato solido

★ **The signal** generated in a silicon detector depends essentially only on the thickness of the depletion zone and on the dE/dx of the particle.

★ **The noise** in a silicon detector system depends on various parameters: geometry of the detector, the biasing scheme, the readout electronics, etc.

★ Noise is typically given as “equivalent noise charge” ENC. This is the noise at the input of the amplifier in elementary charges.

The Charge Signal

- **Collected Charge for a Minimum Ionizing Particle (MIP)** **N = (dE/dx)(x/w)**

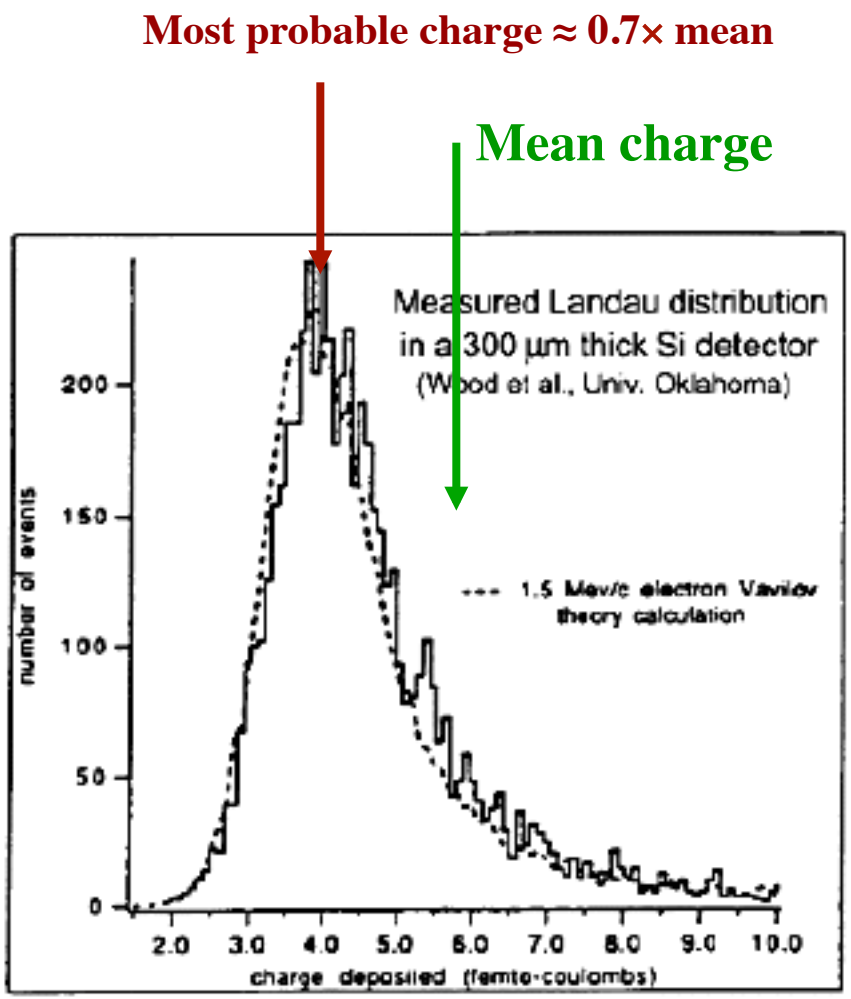
- **Mean energy loss**
  dE/dx (Si) = 3.88 MeV/cm
  ⇒ 116 keV for 300μm thickness

- **Most probable energy loss**
  ≈ 0.7 ×mean
  ⇒ 81 keV

- **3.6 eV to create an e-h pair**
  ⇒ 72 e-h / μm (most probable)
  ⇒ 108 e-h / μm (mean)

- **Most probable charge** (300 μm)

  ≈ 22500 e ≈ 3.6 fC



Signal to noise ratio (S/N)

- **Landau distribution** has a low energy tail
  - becomes even lower by noise broadening

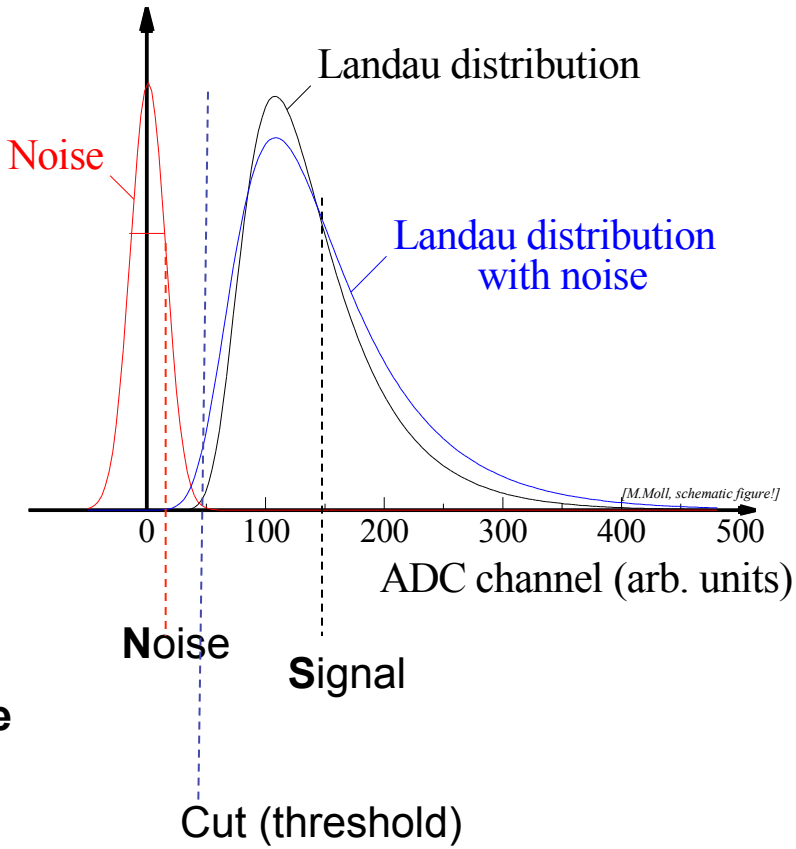
- **Good hits selected by requiring N_{ADC} > noise tail**

  If cut too high ⇒ efficiency loss
  If cut too low ⇒ noise occupancy

- **Figure of Merit: Signal-to-Noise Ratio S/N**

- **Typical values >10-15, people get nervous below 10.**

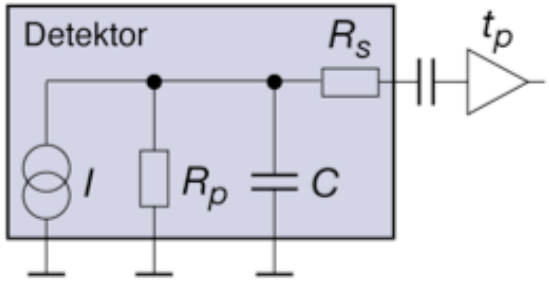
  Radiation damage severely degrades the S/N.



Rivelatori a semiconduttore

The most important noise contributions are:

1. Leakage current (ENC_I)
2. Detector capacity (ENC_C)
3. Det. parallel resistor (ENC_{Rp})
4. Det. series resistor (ENC_{Rs})



Alternate circuit diagram of a silicon detector.

The overall noise is the quadratic sum of all contributions:

$$ENC = \sqrt{ENC_C^2 + ENC_I^2 + ENC_{Rp}^2 + ENC_{Rs}^2}$$

Shot noise

- The detector leakage current comes from thermally generated electron holes pairs within the depletion region. These charges are seperated by the electric field and generate the leakage current. The fluctuations of this current are the source of noise.

In a typical detector system (good detector quality, no irradiation damage) the leakage current noise is usually negligible.

Assuming an amplifier with an integration time ("peaking time") t_p followed by a CR-RC filter the noise contribution by the leakage current I can be written as:

$$\boxed{\mathrm{ENC_I} = \frac{e}{2}\sqrt{\frac{I t_p}{e}}}$$

e .... Euler number (2.718...)
e ... Electron charge

Using the physical constants, the leakage current in units of nA and the integration time in μs the formula can be simplified to:

$$\mathrm{ENC_I} \approx 107\sqrt{I t_p} \qquad [I \text{ in nA},\ t_p \text{ in } \mu\text{s}]$$

To minimize this noise contribution the detector should be of high quality with small leakage current and the integration time should be short.

Capacitance noise

The detector capacity at the input of a charge sensitive amplifier is usually the dominant noise source in the detector system.

This noise term can be written as:

$$\boxed{\mathrm{ENC_C} = a + b \cdot C}$$

The parameter *a* and *b* are given by the design of the (pre)-amplifier. *C* is the detector capacitance at the input of the amplifier channel.

Typical values are (amplifier with ~ 1 μs integration time):

$$a \approx 160 \text{ e und } b \approx 12 \text{ e/pF}$$

To reduce this noise component segmented detectors with short strip or pixel structures are preferred.

Parallel resistor noise

The parallel resistor R_p in the alternate circuit diagram is the bias resistor. The noise term can be written as:

$$\boxed{\mathrm{ENC}_{\mathrm{Rp}} = \frac{\mathscr{e}}{e}\sqrt{\frac{kTt_p}{2R_p}}}$$

$\mathscr{e}$ .... Euler number (2.718...)
e ... Electron charge

Assuming a temperature of T=300K, t_p in μs and R_p in MΩ the formula can be simplified to:

$$\mathrm{ENC}_{\mathrm{Rp}} \approx 772\sqrt{\frac{t_p}{R_p}} \qquad \left[R_p \text{ in M}\Omega,\ t_p \text{ in } \mu\text{s}\right]$$

To achieve low noise the parallel (bias) resistor should be large!

However the value is limited by the production process and the voltage drop across the resistor (high in irradiated detectors).

Serie resistor noise

The series resistor R_s in the alternate circuit diagram is given by the resistance of the connection between strips and amplifier input (e.g. aluminum readout lines, hybrid connections, etc.). It can be written as:

$$\boxed{\mathrm{ENC}_{\mathrm{Rs}} \approx 0.395\, C \sqrt{\frac{R_s}{t_p}}}$$

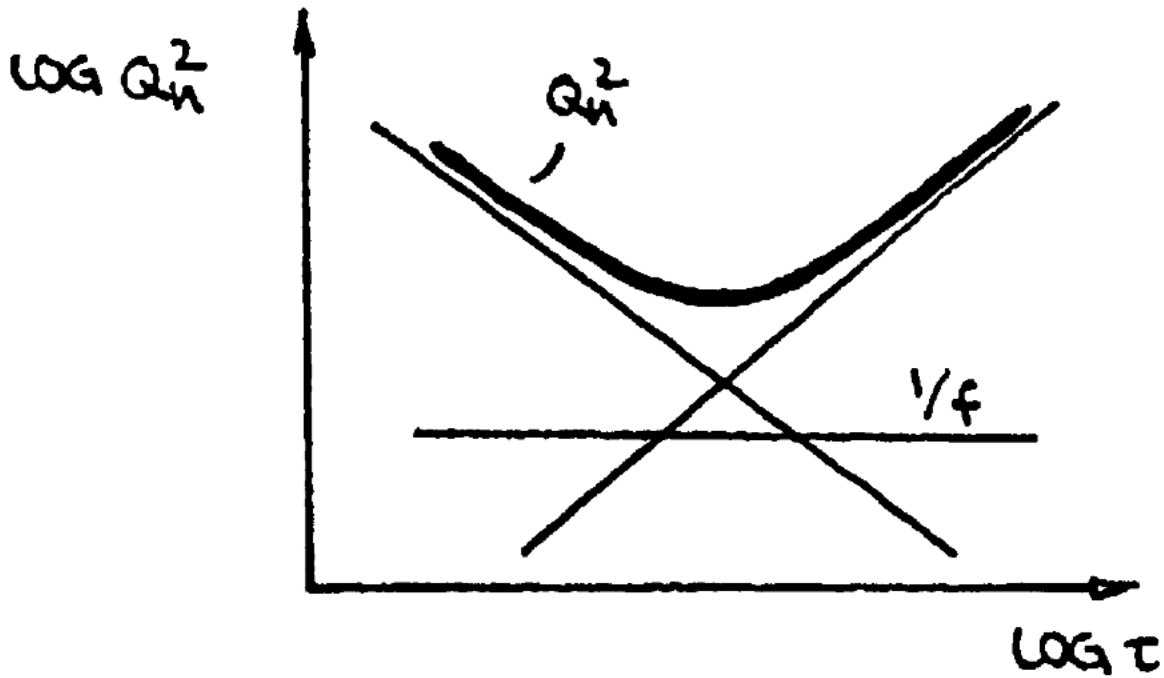
C .... Detector capacity on pF
t_p ... Integration time in μs
R_s ... Series resistor in Ω

Note that, in this noise contribution t_p is inverse, hence a long t_p reduces the noise. The detector capacitance is again responsible for larger noise.

To avoid excess noise the aluminum lines should have low resistance (e.g. thick aluminum layer) and all other connections as short as possible.

Q_n assumes a minimum when the current and voltage noise contributions are equal.

dominated by voltage current noise



To achieve a high signal to noise ratio in a silicon detector system the following conditions are important:

- ★ Low detector capacity (i.e. small element size)
- ★ Low leakage current
- ★ Large bias resistor
- ★ Short and low resistance connection to the amplifier
- ★ Usually long integration time

Obviously some of the conditions are contradictory. Detector and front end electronics have to be designed as one system. The optimal design depends on the application.

DELPHI Microvertex:	CMS Tracker:
★ readout chip (MX6): $a = 325$ e, $b = 23$ e/pF, $t_p = 1.8\ \mu s$	★ readout chip (APV25, deconvolution): $a = 400$ e, $b = 60$ e/pF, $t_p = 50$ ns
★ 2 detectors in series each 6 cm long strips, $C = 9$ pF → $ENC_C = 532$ e	★ 2 detectors in series each 10 cm long strips, $C = 18$ pF → $ENC_C = 1480$ e
★ typ. leakage current/strip: $I \approx 0.3$ nA → $ENC_I = 78$ e	★ max. leakage current/strip: $I \approx 100$ nA → $ENC_I = 103$ e
★ bias resistor $R_p = 36$ MΩ → $ENC_{Rp} = 169$ e	★ bias resistor $R_p = 1.5$ MΩ → $ENC_{Rp} = 60$ e
★ series resistor = 25 Ω → $ENC_{Rs} = 13$ e	★ series resistor = 50 Ω → $ENC_{Rs} = 345$ e
→ **Total noise: ENC = 564 e (SNR 40:1)**	→ **Total noise: ENC = 1524 e (SNR 15:1)**

Calculated for the signal of a minimum ionizing particle (mip) of 22500 e.

Si detectors: typical noise performance

- Example of noise
  - Some typical values for LEP silicon strip modules (OPAL):
    - ENC = 500 + 15 $\cdot C_d$
    - Typical strip capacitance is about 1.5pF/cm, strip length of 18cm so C_d=27pF

so ENC = 900e. Remember S=22500e

$\Rightarrow$ S/N $\approx$ 25/1

- Some typical values for LHC silicon strip modules
  - ENC = 425 + 64 $\cdot C_d$
  - Typical strip capacitance is about 1.2pF/cm, strip length of 12cm so C_d=14pF

so ENC = 1300e

$\Rightarrow$ S/N $\approx$ 17/1

Capacitive term is much worse for LHC in large part due to very fast shaping time needed (bunch crossing of 25ns vs 22μs for LEP)

Risoluzione spaziale

The position resolution – the main parameter of a position detector – depends on various factors, some due to physics constraints and some due to the design of the system (external parameters).

★ Physics processes:

- Statistical fluctuations of the energy loss
- Diffusion of charge carriers

★ External parameter:

- Binary readout (thresh hold counter) or read out of analogue signal value
- Distance between strips (strip pitch)
- Signal to noise ratio

Risoluzione spaziale

★ Silicon position detectors are thin (300–500 μm) and absorb only a small fraction of the total energy of through going particles.

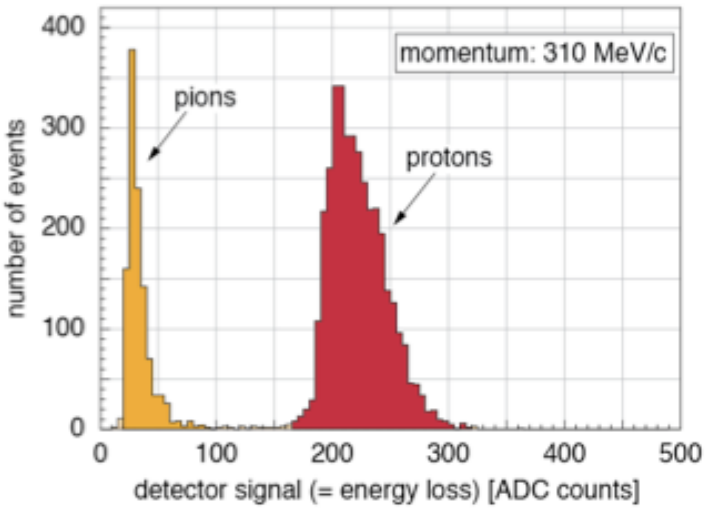
★ The energy loss *dE/dx* follows a Landau distribution, an asymmetric probability function with a long "tail" to large energy deposits.

★ Example of a mip measured in a 300 μm thick silicon detector:

- Most probable energy loss (Maximum of the distribution): 78 keV in 300 μm → ≈ 72 e^-h^+ pairs per μm

- Mean of the energy loss: 116 keV in 300 μm → ≈ 108 e^-h^+ pairs per μm

Pions and Protons:



W. Adam et al., CMS note 1998/092 (1998)

Risoluzione spaziale

Long tail in energy loss distribution is due to δ-electrons.
δ-electrons have a high energy (keV) and are produced by rare, hard collisions between incident particle and electrons from the detector material.

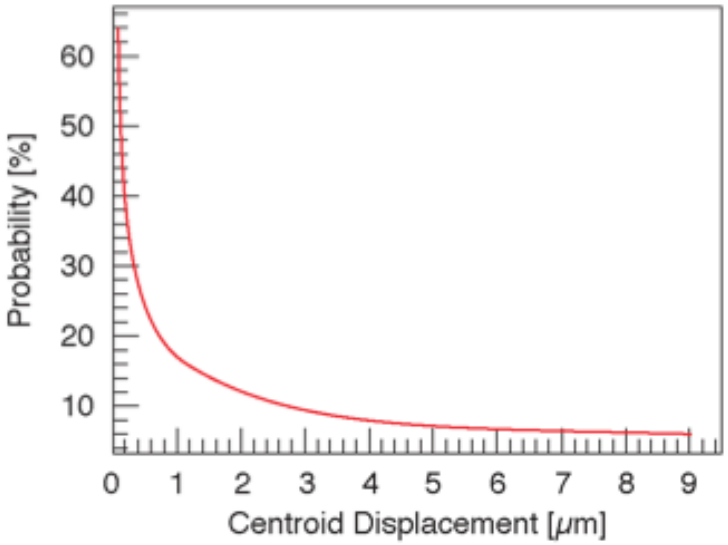
★ The probability to produce a δ-electrons is small.

★ δ-electrons have a long track length in the detector material and may produce e^+h^- pairs along the track.

➔ Dislocate the measured track

➔ Measurement errors in the order of μm unavoidable

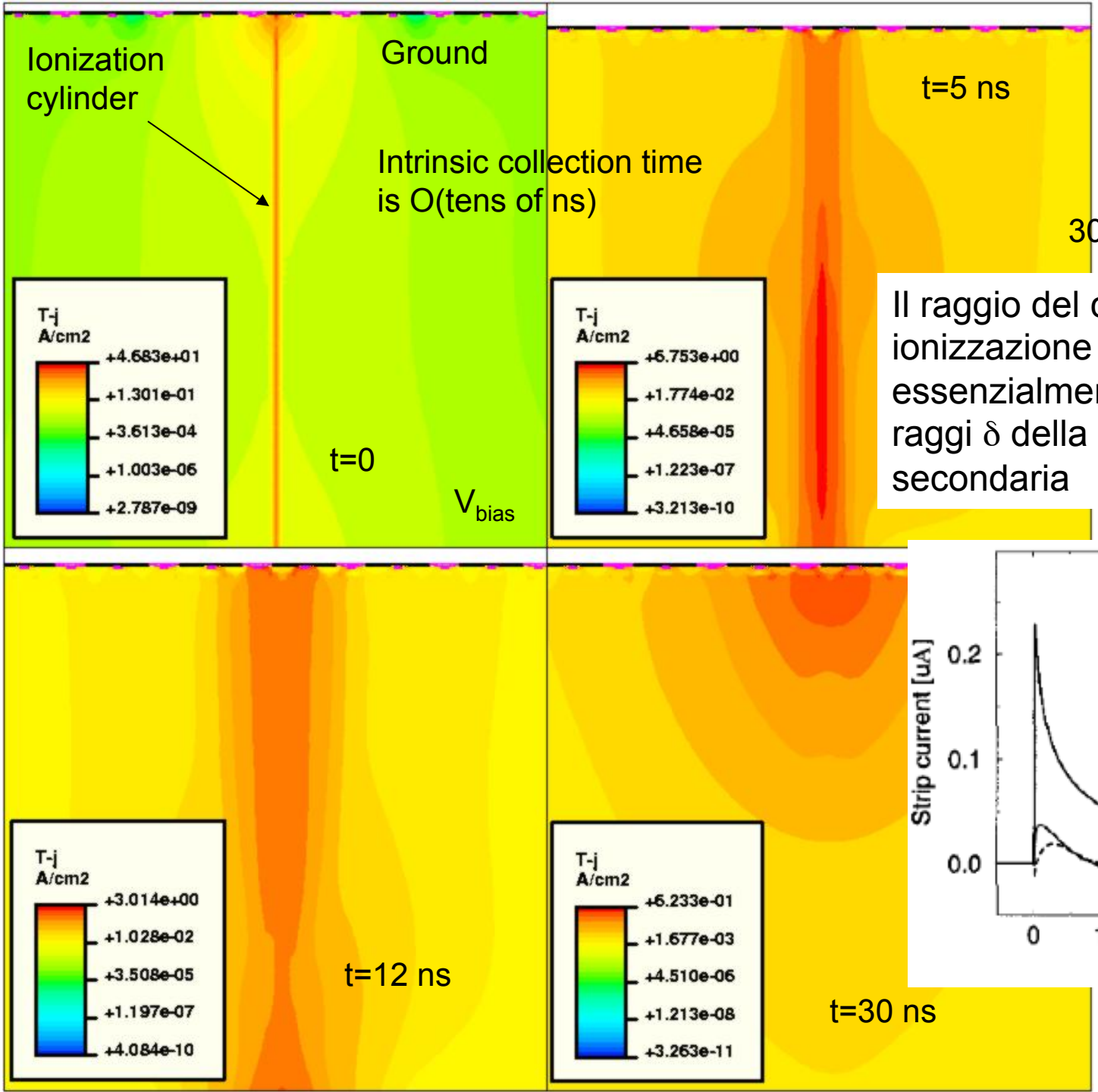
Displacement probability (calculation) of the charge center of gravity due to δ-electrons:



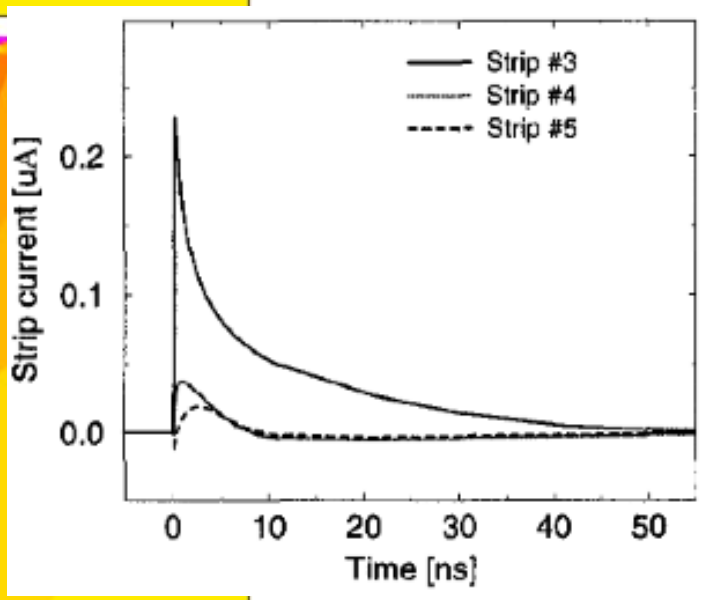
A. Peisert, *Silicon Microstrip Detectors*, DELPHI 92-143 MVX 2, CERN, 1992

Time evolution of current

300 um Silicon layer



Il raggio del cilindro di ionizzazione e' fissato essenzialmente dal range dei raggi δ della ionizzazione secondaria



Equazione di diffusione

The convection–diffusion equation can be derived in a straightforward way from the continuity equation, which states that the rate of change dn/dt for a scalar quantity in a differential control volume is given by flow and diffusion into and out of that part of the system along with any generation or destruction inside the control volume:

$$\frac{\partial n}{\partial t} + \nabla \cdot \vec{j} = R,$$

where is the total flux and *R* is a net volumetric source for n There are two sources of flux in this situation. First, **diffusive flux** arises due to diffusion. This is typically approximated by Fick's first law:

$$\vec{j}_{\text{diffusion}} = -D \nabla n$$

i.e., the flux of the diffusing material (relative to the bulk motion) in any part of the system is proportional to the local concentration gradient. Second, when there is overall convection or flow, there is an associated flux called **advective flux**: $\vec{j}_{\text{advective}} = \vec{v}\, n$

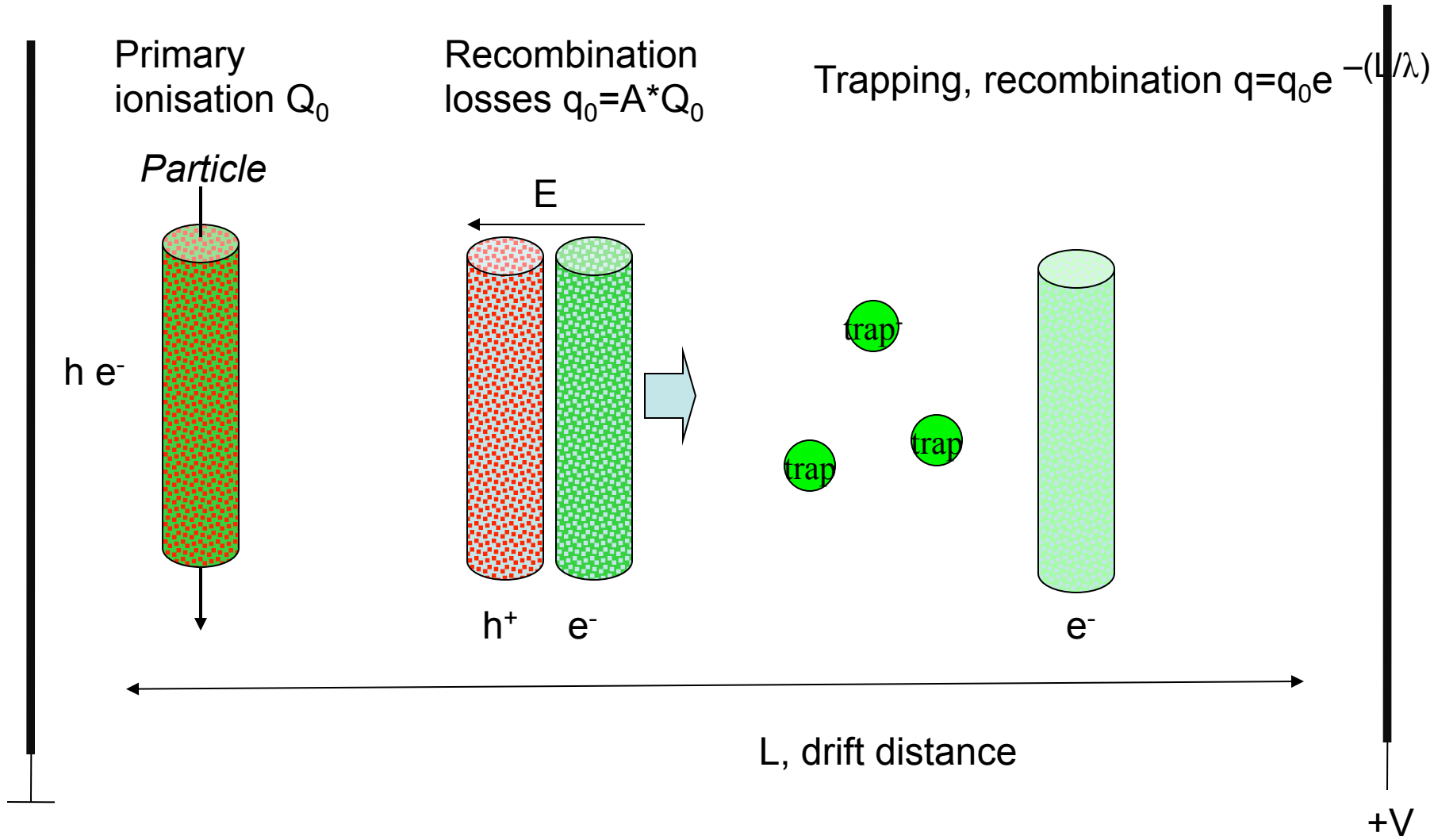
The total flux is given by the sum of these two: $\vec{j} = \vec{j}_{\text{diffusion}} + \vec{j}_{\text{advective}} = -D \nabla n + \vec{v}\, n$

$$\frac{\partial n}{\partial t} + \nabla \cdot \left(-D \nabla n + \vec{v}\, n\right) = R.$$

As for gas: initial volume recomb and Q loss during their travel to electrodes

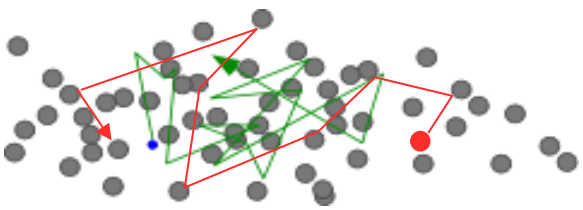
Response to a primary ionization

$$\frac{\partial n}{\partial t} + \nabla \cdot \left(-D \nabla n + \vec{v}\, n\right) = R.$$

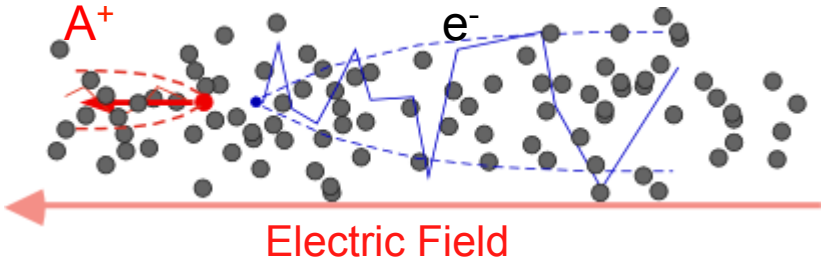


Drift and Diffusion in Presence of E field

E=0 thermal diffusion $\langle v \rangle_t = 0$



E>0 charge transport and diffusion $\langle v \rangle_t = v_D$



The solution of diffusion-transport eqn is then

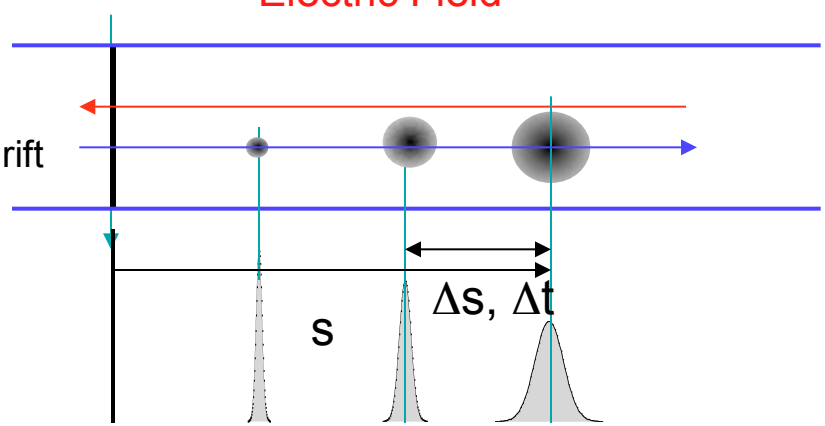
$$N(x,t) = \frac{N_0}{\sqrt{4\pi Dt}} e^{-\frac{(x-v_D t)^2}{4Dt}}$$

Electron swarm drift

Drift velocity

Diffusion

$v_D = \mu E$



drift + diffusion motion: the average position of the charge swarm moves as x = vt, while the width increases in time

Charge Collection time and diffusion

- **Charge Collection time**
  - Drift velocity of charge carriers $v \approx \mu E$, so drift time, $t_d = d/v = d/\mu E$

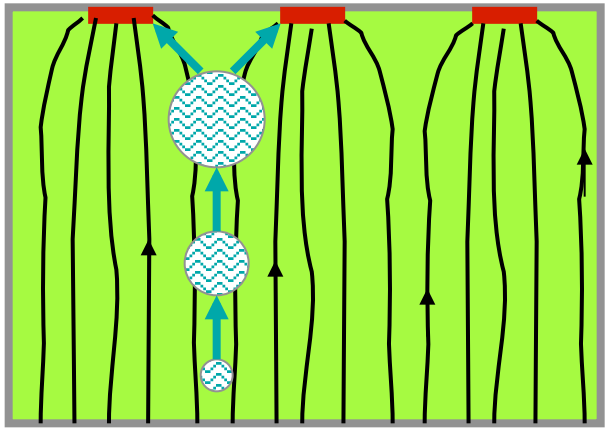
Typical values: d=300 μm, E= 2.5 kV/cm,
with μ_e= 1350 cm² / V·s and μ_h= 450 cm² / V·s

$$\Rightarrow \mathbf{t_d(e)= 9ns\ ,\ t_d(h)= 27ns}$$

- **Diffusion**
  - Diffusion of charge "cloud" caused by scattering of drifting charge carriers, radius of distribution after time td:

$\sigma = \sqrt{2Dt_d}$ with diffusion constant $D = \mu kT/q$

- Same radius for e and h since $t_d \propto 1/\mu$
  Typical charge radius: $\sigma \approx 6\mu m$, could exploit this to get better position resolution due to charge sharing between adjacent strips (using centroid finding), but need to keep drift times long (low field).

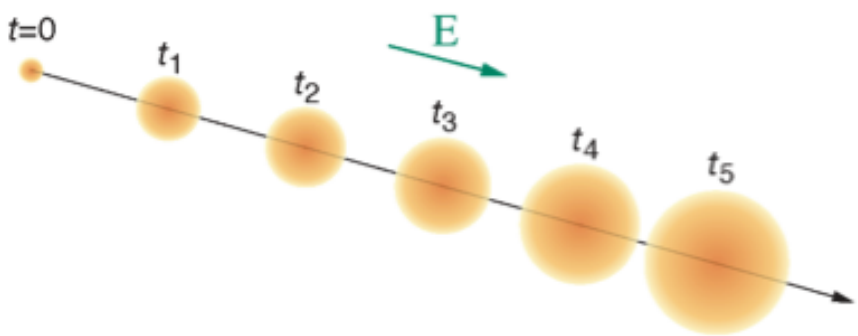


Risoluzione spaziale

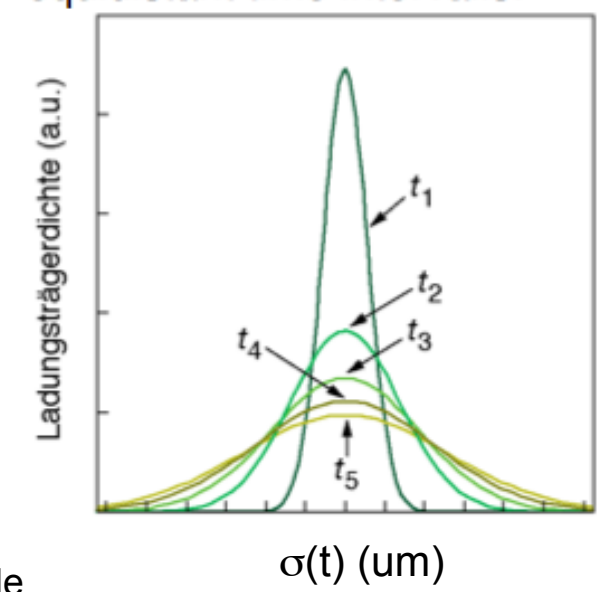
★ h^+ created close to the anode (i.e. the n^+ backplane) and e^- created close to he cathode (i.e. the p^+ strips or pixels) have the longest drift path. As a consequence the diffusion acts much longer on them compared to e^- h^+ with short track paths.

➔ The signal measured comes from many overlapping Gaussian distributions.

Drift and diffusion acts on charge carriers:



Charge density distribution for 5 equidistant time intervalls:



Risoluzione spaziale

★ Diffusion widens the charge cloud. However, this has an positive effect on the position resolution!

➔ charge is distributed over more than one strip, with interpolation (calculation of the charge center of gravity) a better position measurement is achievable.

★ This is only possible if analogue read out of the signal is implemented.

★ Interpolation is more precise the larger the signal to noise ratio is.

➔ Strip pitch and signal to noise ratio determine the position resolution.

★ Larger charge sharing can also be achieved by tilting the detector.

Risoluzione spaziale

★ Threshold readout (one strip signal):

→ position: $x = \text{strip position}$

→ resolution: $\sigma_x \approx \frac{p}{\sqrt{12}}$

p … distance between strips (readout pitch)

x … position of particle track

★ charge center of gravity (signal on two strips):

→ position:

$$x = x_1 + \frac{h_2}{h_1 + h_2}(x_2 - x_1) = \frac{h_1 x_1 + h_2 x_2}{h_1 + h_2}$$

→ resolution: $\sigma_x \propto \frac{p}{SNR}$

x_1, x_2 … position of 1st and 2nd strip

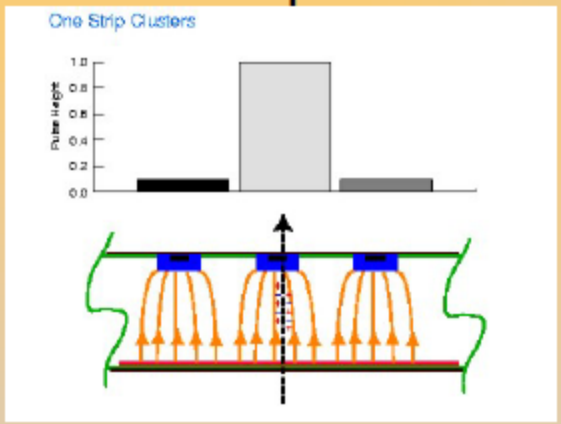
h_1, h_2 … signal on 1st and 2nd strip

SNR … signal to noise ratio

Si strip detectors: spatial resolution

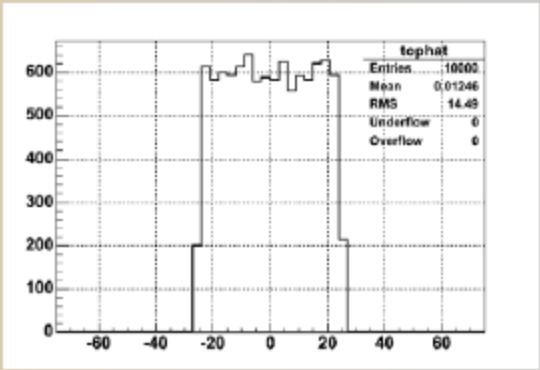
Resolution is the spread of the reconstructed position minus the true position

For one strip clusters

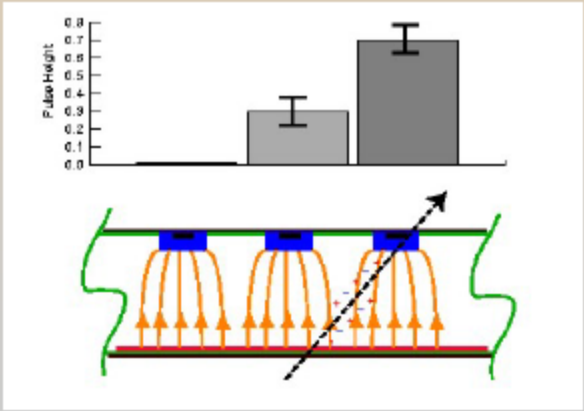


$$\sigma = \frac{\text{pitch}}{\sqrt{12}}$$

"top hat" residuals

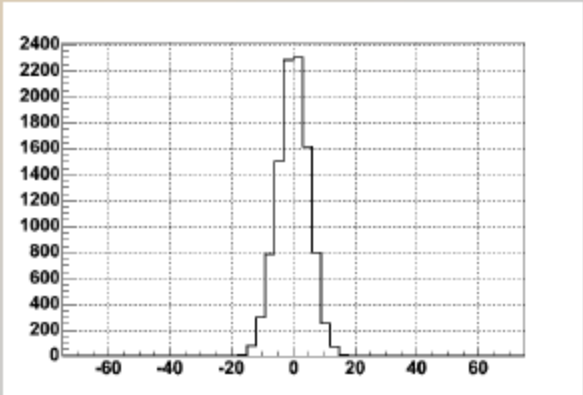


For two strip clusters



$$\sigma \approx \frac{\text{pitch}}{1.5 * (S/N)}$$

"gaussian" residuals



Risoluzione spaziale

★ The strip pitch determines to a large extend the position resolution. With small strip pitch a better position resolution is achievable.

But the signal-to-noise ratio plays an essential role

- ➔ small strip pitch requires large number of electronic channels
- ➔ cost increase
- ➔ power dissipation increase

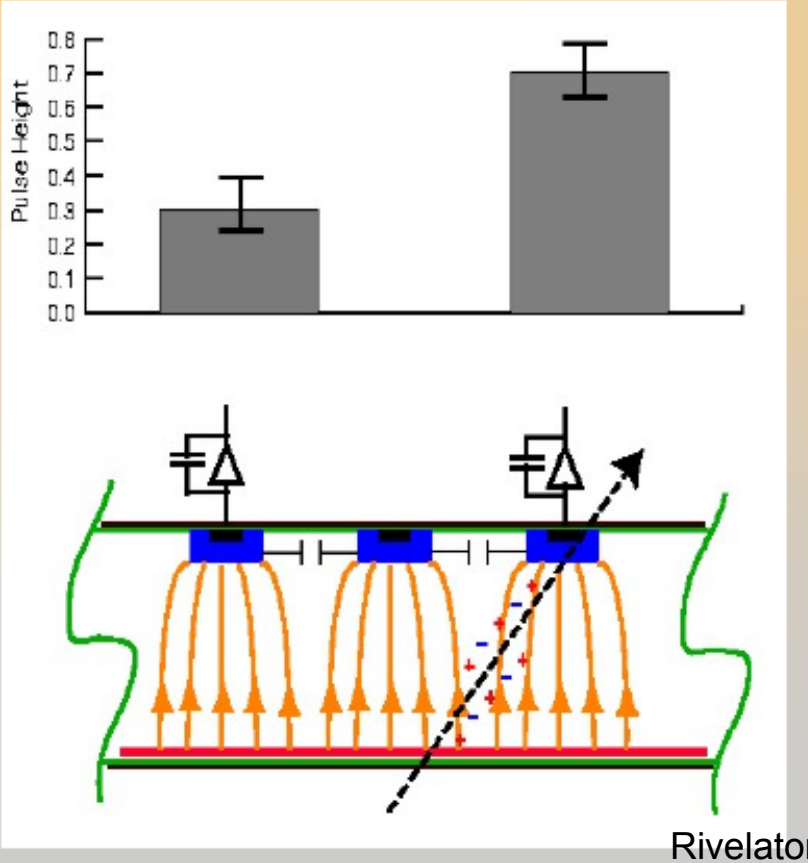
★ A possible solution is the implementation of intermediate strips. These are strips not connected to the readout electronics located between readout strips.

The signal from these intermediate strips is transferred by capacitive coupling to the readout strips.

- ➔ more hits with signals on more than one strip
- ➔ Improved resolution with smaller number of readout channels.

Si strip detectors: spatial resolution

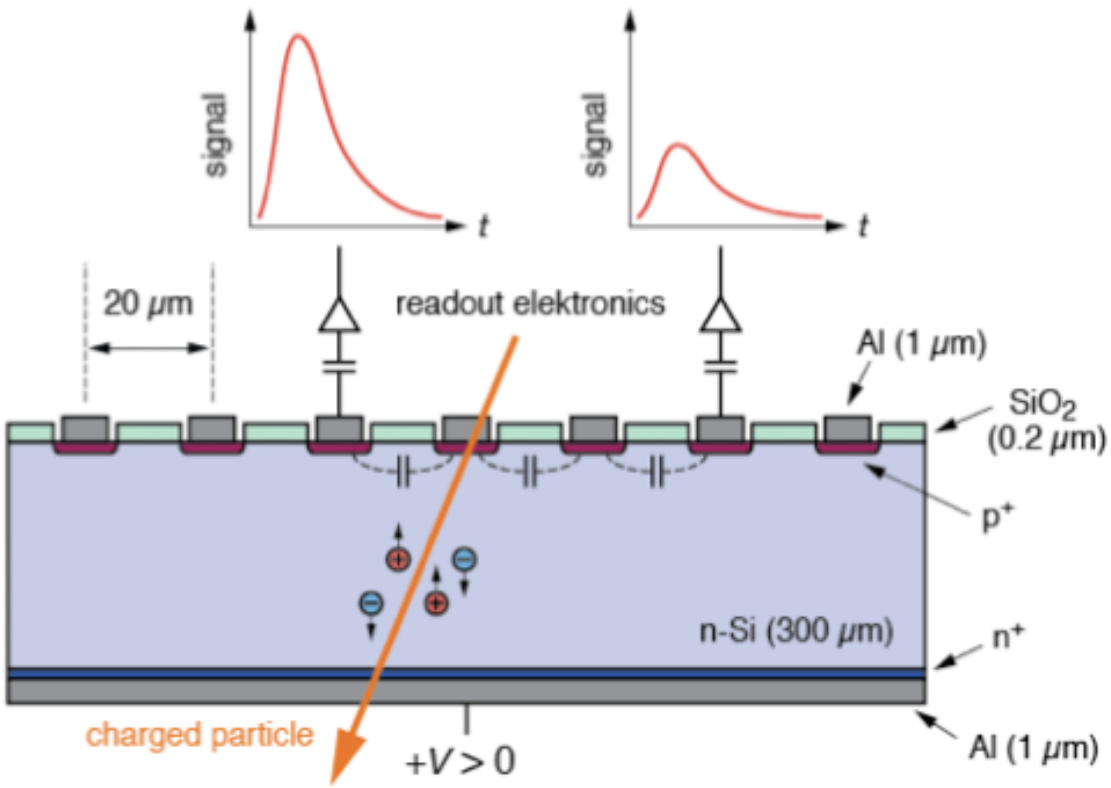
Fine pitch is good… but there is a price to pay! $$$$$
The floating strip solution can help



- The charge is shared to the neighboring strips via capacitative coupling. We don't have to read out every strip but we still get great resolution
- This is a very popular solution. ALEPH for instance obtain $\sigma \approx 12\ \mu m$ using a readout pitch of 100 μm and an implant pitch of 25 μm
- But you can't have everything for nothing! You can lose charge from the floating strips to the backplane, so you must start with a good signal to noise

Risoluzione spaziale

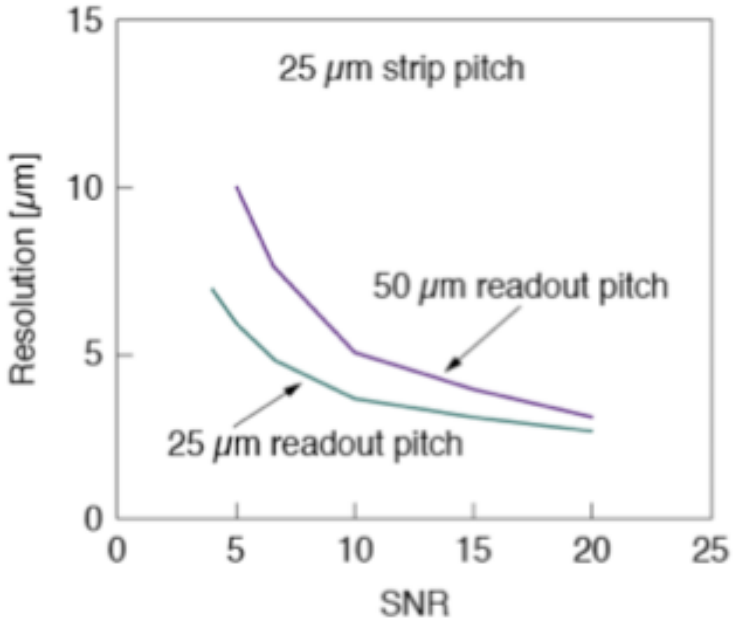
Scheme of a detector with two intermediate strips. Only every 3rd strip is connected to an electronics channel. The charge from the intermediate strips is capacitive coupled to the neighbor strips.



Risoluzione spaziale

Example of a detector with strip pith of 25 μm and analogue readout.
The position resolution is plotted as a function of the SNR.

Bottom curve: every strip is connected to the readout electronics
Top curve: every 2nd strip is connected, one intermediate strip



A. Peisert, *Silicon Microstrip Detectors*, DELPHI 92-143 MVX 2, CERN, 1992

To benefit from intermediate strips large SNR is required!

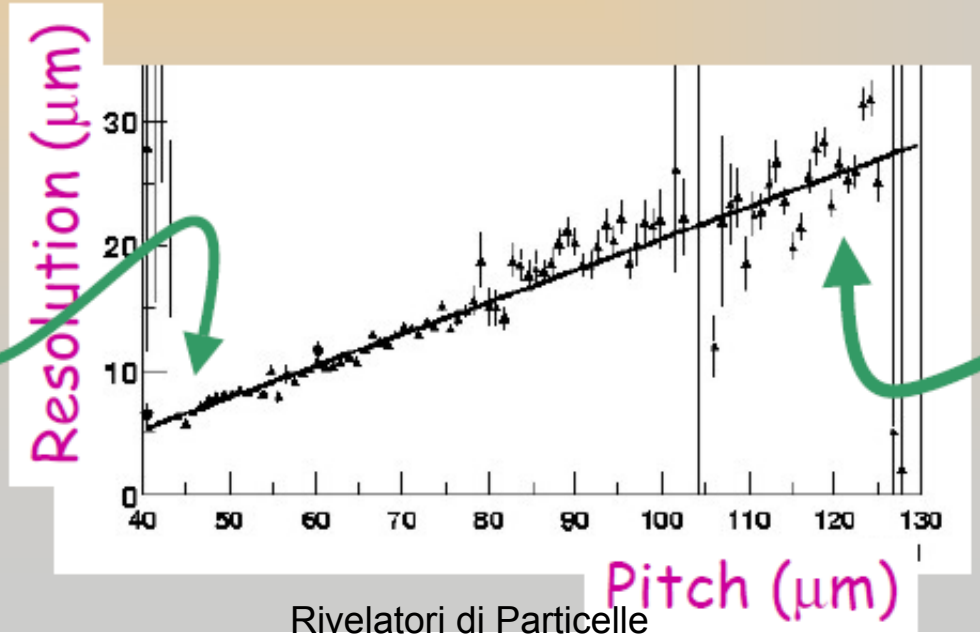
Si strip detectors: spatial resolution

In real life, position resolution is degraded by many factors

- relationship of strip pitch and diffusion width (typically 25-150 μm and 5-10 μm)
- Statistical fluctuations on the energy deposition

Typical real life values for a 300μm thick sensor with S/N=20

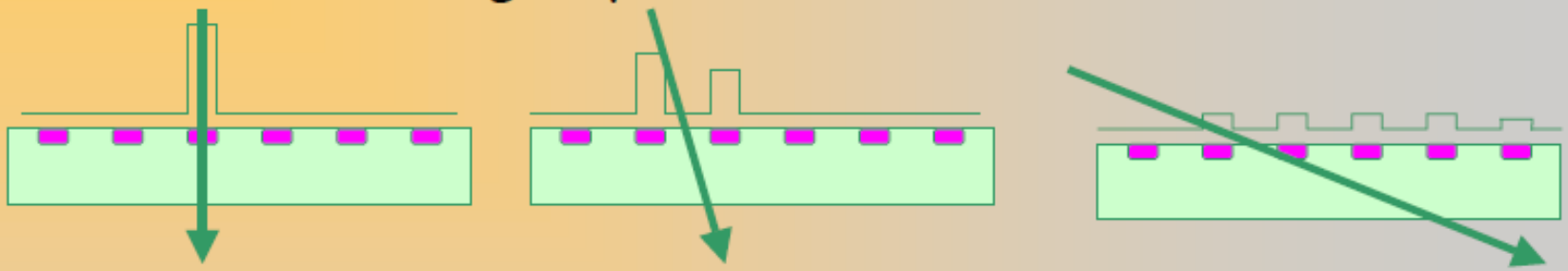
Here charge sharing dominates

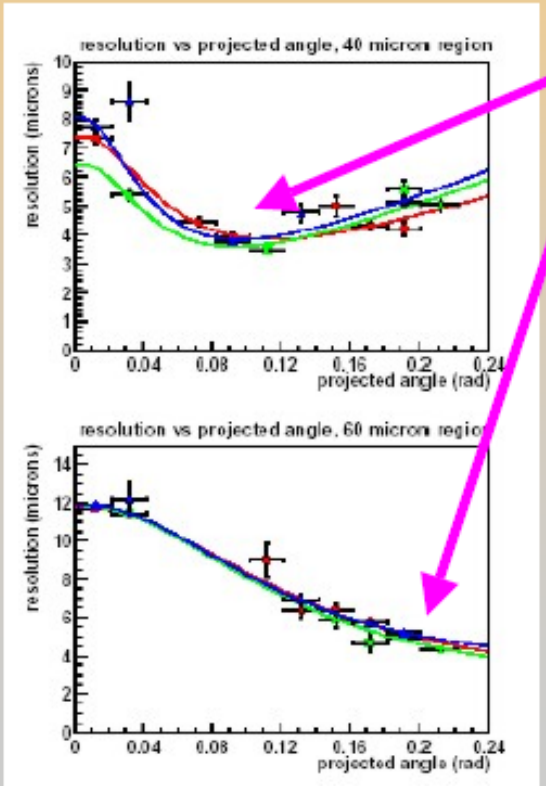


Here single strips dominate

Si strip detectors: spatial resolution

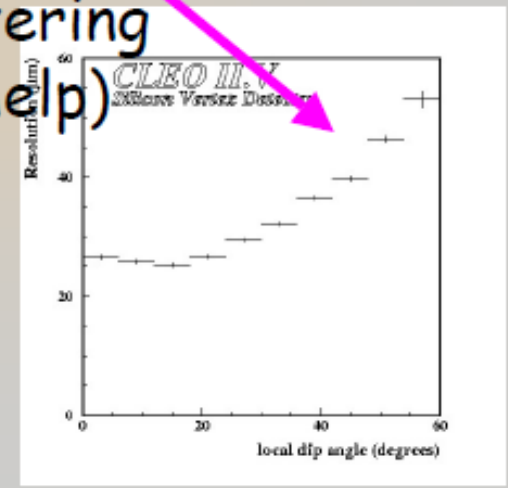
There is also a strong dependence on the track incidence angle





At small angles you win

At large angles you lose (but a good clustering algorithm can help)



Optimum is at

$$\tan^{-1}\frac{\text{pitch}}{\text{width}}$$

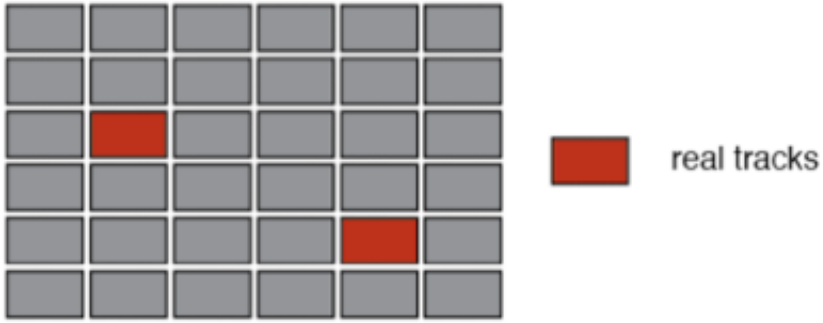
Pixel detector

★ Double sided strip sensors measure the 2 dimensional position of a particle track. However, if more than one particle hits the strip detector the measured position is no longer unambiguous. “Ghost”-hits appear!

True hits and ghost hits in a double sided strip detector in case of two particles traversing the detector:

★ Pixel detectors produce unambiguous hits!

Measured hits in a pixel detector in case of two particles traversing the detector:



Pixel detector

- ★ Typical pixel size is 50 μm x 50 μm.
- ★ If signal pulse height is not recorded, resolution is the digital resolution: appr. 14 μm (50 μm pixel pitch).

$$\sigma_x \approx \frac{d}{\sqrt{12}}$$

d … pixel dimension

Better resolution achievable with analogue readout

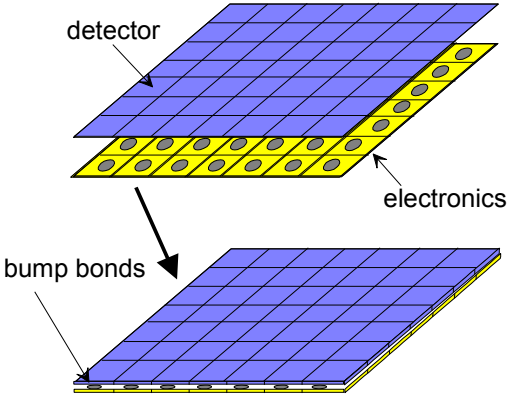
- ★ Small pixel area ➔ low detector capacitance (≈1 fF/Pixel) ➔ large signal-to-noise ratio (e.g. 150:1).
- ★ Small pixel volume ➔ low leakage current (≈1 pA/Pixel)

- ★ Drawback of hybrid pixel detectors: Large number of readout channels
  - ➔ Large number of electrical connections in case of hybrid pixel detectors.
  - ➔ Large power consumption of electronics..

Rivelatori a stato solido

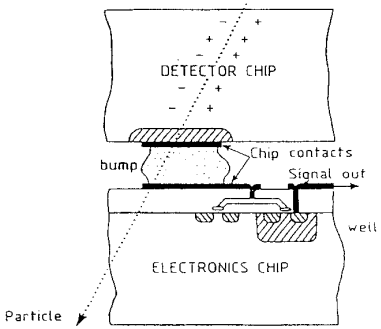
Pixel

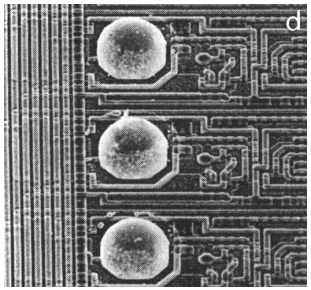
- Si segmenta il Si a quadratini
- Elettronica con la stessa geometria



RD 19, E. Heijne et al., NIM A 384 (1994) 399

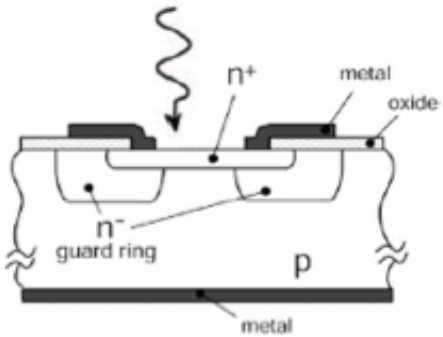
Flip-chip technique



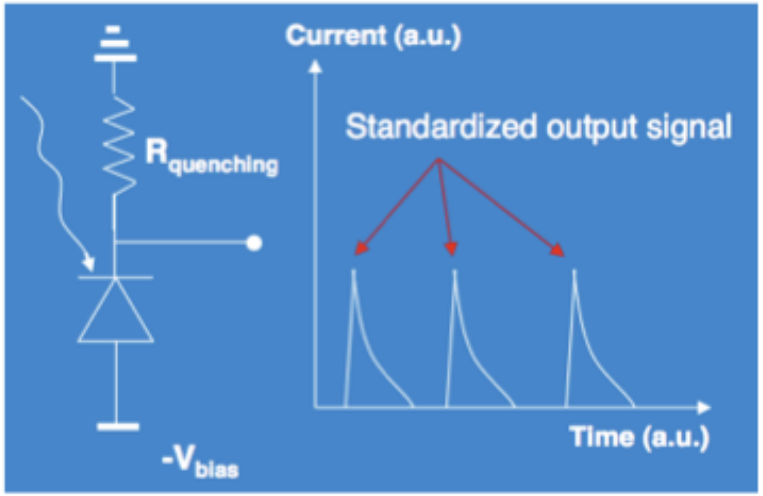


- Requires sophisticated readout architecture
- First experiment WA94 (1991), WA97
- OMEGA 3 / LHC1 chip (2048 pixels, 50x500 μm^2) (CERN ECP/96-03)
- Pixel detectors will be used in all future LHC experiments

APD are diodes operated in reverse bias mode above the breakdown voltage.



R. H. Haitz, J. App.Phys. Vol. 36, No. 10 (1965) 3123



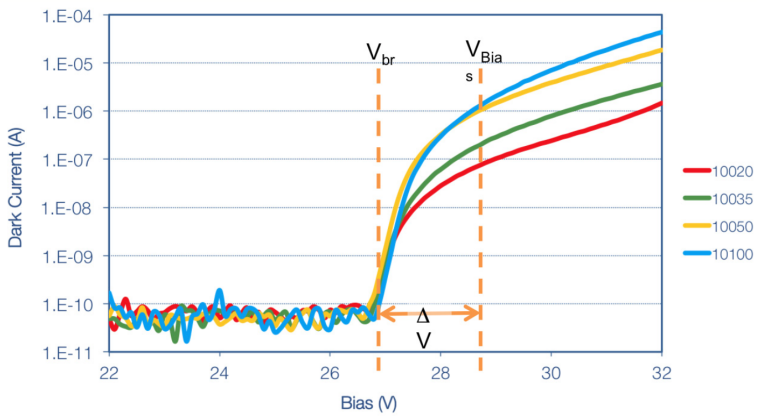
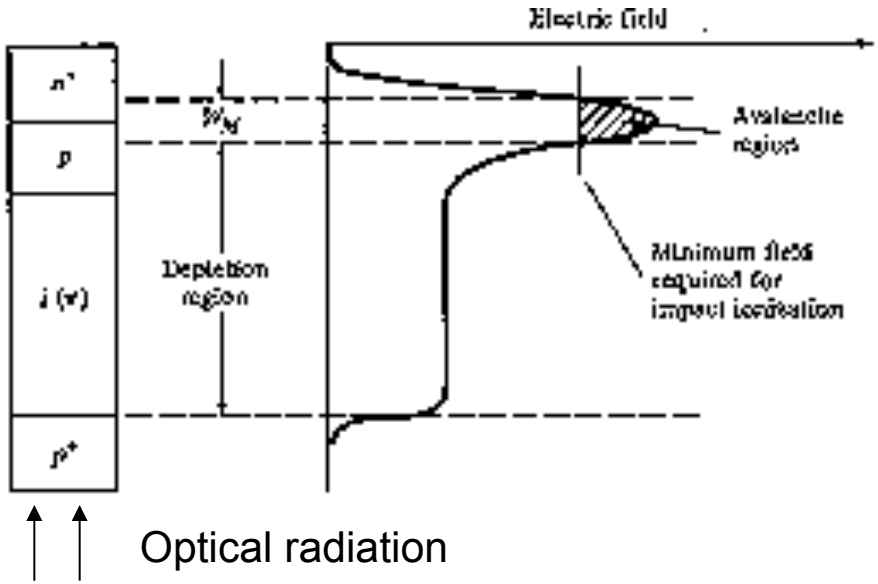


Figure 7 Dark current as a function of voltage for 1mm SPMs of various microcell size (20um, 35um, 50um, 100um).

The diode, operated above the breakdown voltage, is in a”meta-stable” state.
Until some charge is generated in the avalanche regione the current stays stable
Thermal generation is a noise source.
A single photon is able to trigger an avalanche.
The avalanche gives a large current pulse above the DC current.
Counting the pulses, photons are counted (taking into accunt conversion probability, detection efficiency,…).
No proporzionality, it’s a binary device.
If two photons hit the APD, the output signal is the same

Avalanche Photodiode (APD)

APDs internally multiply the primary photocurrent before it enters to following circuitry.
In order to carrier multiplication take place, the photogenerated carriers must traverse along a high field region. In this region, photogenerated electrons and holes gain enough energy to ionize bound electrons in VB upon colliding with them. This multiplication is known as **impact ionization**. The newly created carriers in the presence of high electric field result in more ionization called **avalanche effect**.



Reach-Through APD structure (RAPD) showing the electric fields in depletion region and multiplication region.

Principle of operation

1. photon is absorbed in the depleted semiconductor
2. photo electron drifts into high field region and initiates an avalanche breakdown
3. passive quenching by resistor (few MΩ)
4. deadtime $\approx 10^{-7}$ s given by the time constant to recharge the pixel ‘s capacity

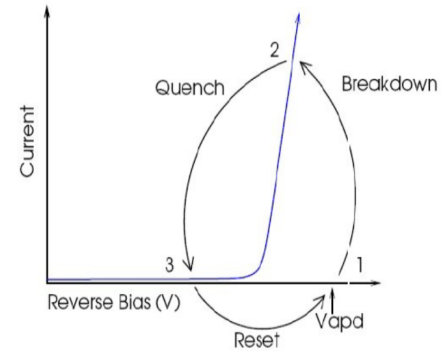
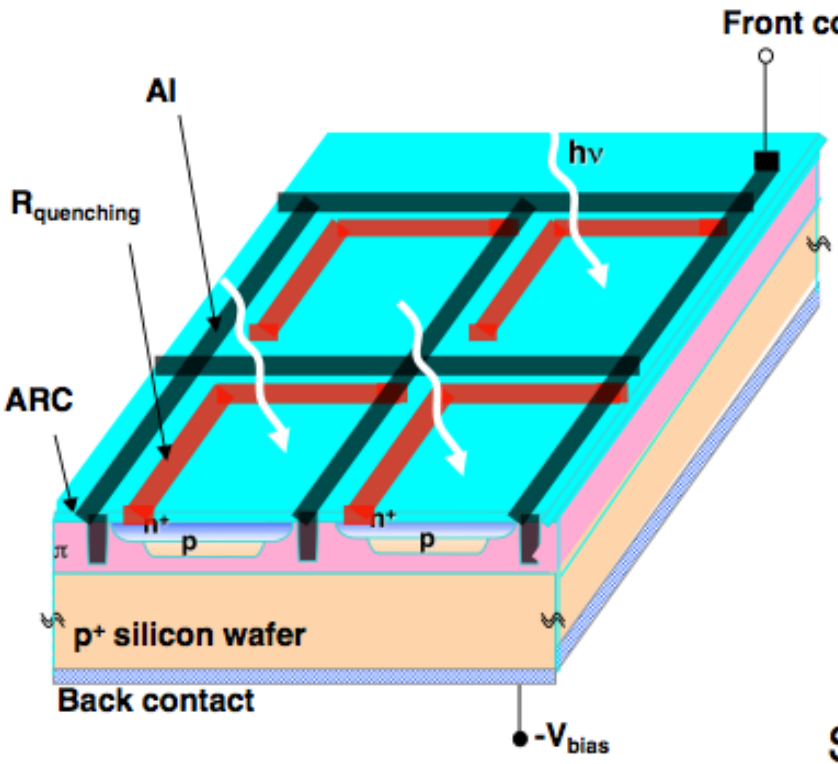


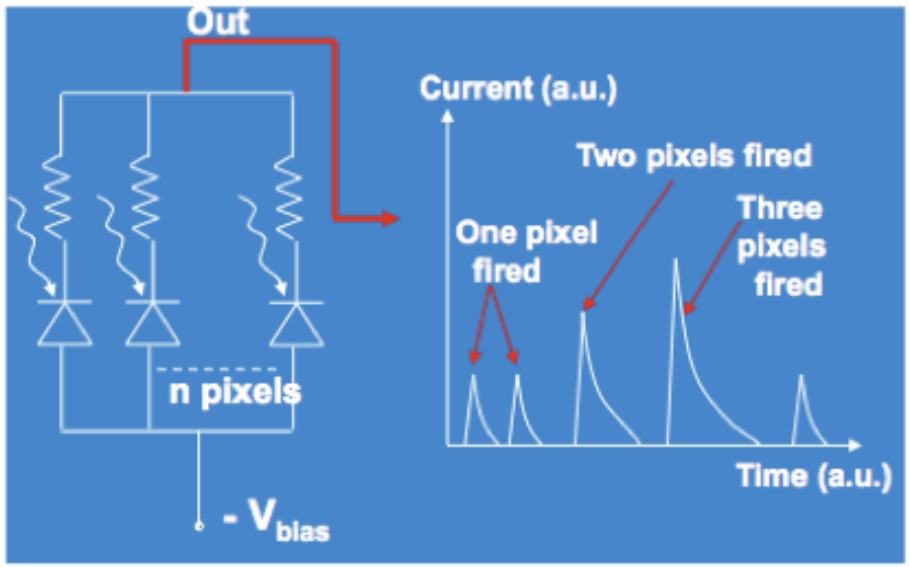
Figure 3a) Breakdown, quench and reset cycle of a photodiode working in the Geiger mode.

The avalanche initiated by the pe increases the current while the V stays constant (due to to the slope of IV in geiger mode, almost vertical).
The voltage drop in the quenching resistor $\Delta i R_q$ decreases the voltage at the diode below breakdown threshold and avalanche stops.
The diode recharges at the working voltage after a time depending on cell capacity and quenching R, $\tau = CR_q$.

SiPM

SiPM are matrices of APDs (up to 50.000) connected in parallel to a common bias line and a common readout line.
The output signal is the sum of the output of all the APDs which fired in the array: the output signal is proportional the number of photons hitting the SiPM





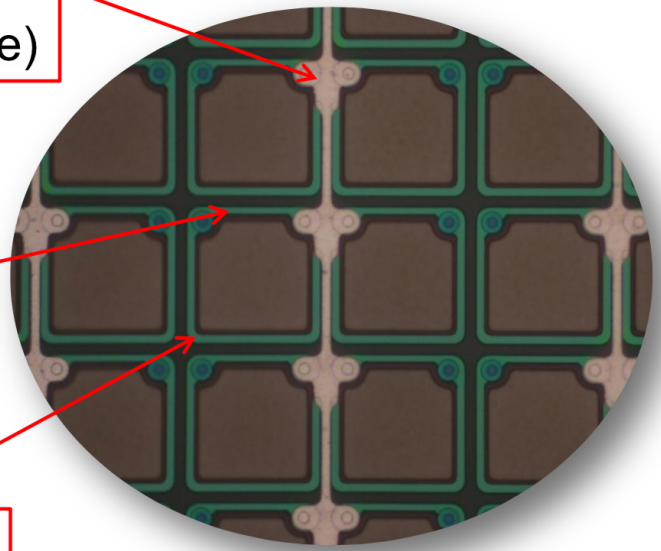
SiPM become more and more popular as replacement for standard photo multiplier.

Device Layout: example of internal structure

Metal line connecting all cells in parallel (one common anode)

Polysilicon resistor

Field-plate to reduce electric field around the junction



The cathode is contacted on the rear side.

Resistor is located around the active area => no fill factor loss

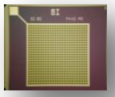
FF ~ 25% 25x25um2 cell
~ 45% 40x40um2 cell
~ 55% 50x50um2 "
~ 72% 100x100um2 "

Device Layout: examples of SiPM geometries

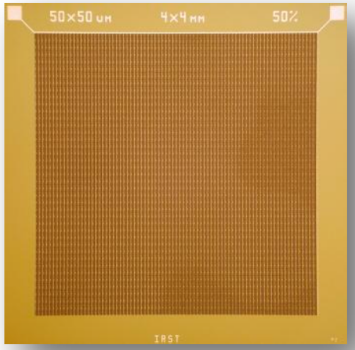
Scale of the pictures is the same

2006

1x1mm^2
40x40μm^2 cell



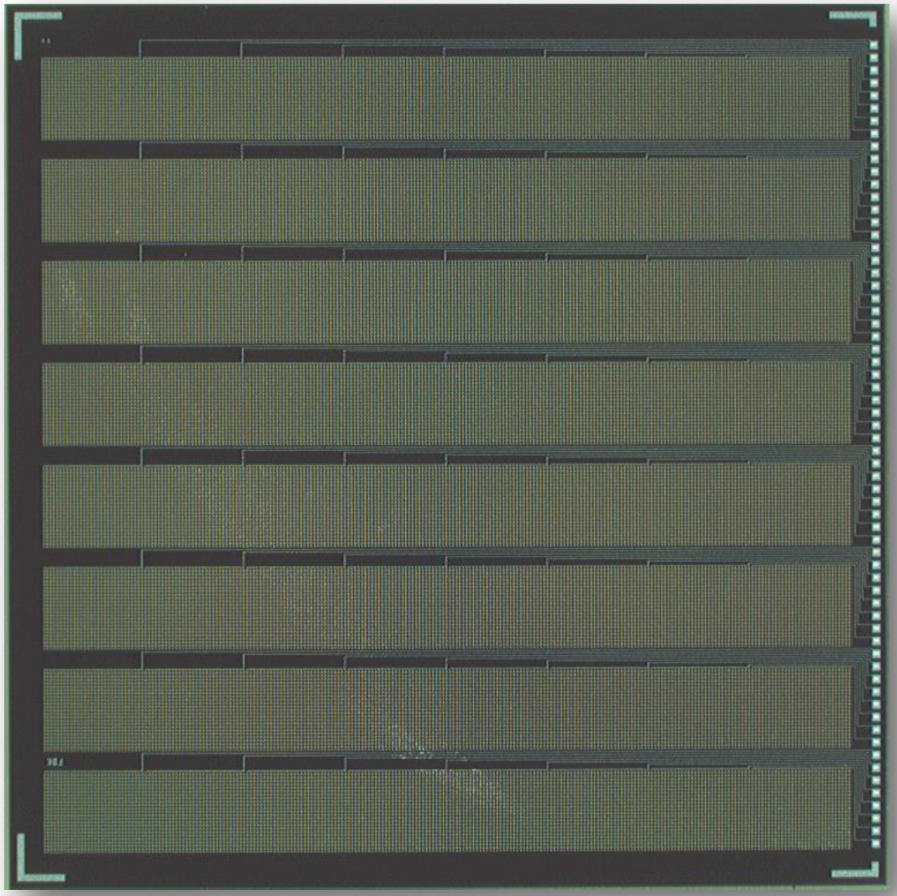
2007



4x4mm^2
50x50μm^2 cell

2009

8x8 array of SiPMs
1.5x1.5mm pitch
50x50μm^2 cell



Produced for DaSiPM (INFN project)

Paragone di apparati per misure di posizione

A seconda dell'applicazione vengono usati diversi apparati per le misure di posizione. C'è la tendenza ad andare a tempi di ripetizione sempre più corti ed a rate sempre più alti (10^9/s ad LHC)

➔

rivelatori al Silicio se reggono il rate.

Per anelli e^+e^- con basso rate di eventi va benissimo una camera cilindrica (a deriva o TPC o jet chamber).

Ogni esperimento necessita del suo apparato a seconda di quello che si vuole misurare.

Sviluppo di apparati ancora in corso, ogni anno novità.

Rivelatori al silicio

- Solidi → autosostenenti (no strutture complicate)
- Alta granularita' (segmentazione 20 -50 um)
- Alta risoluzione spaziale (3-10 um)
- Alta risoluzione in energia
- Risposta temporale veloce (Tc < 30 ns)
- No alta tensione (Vbias < 200 V)
- Funzionano bene nel vuoto e in campi B
- Buona rad hard
- Costi contenuti
- Tecnologia di costruzione solida

Paragone di apparati per misure di posizione

Apparato	Risoluzione (μm)	Tempo morto (ms)	Tempo sensibilità (ns)	Tempo lettura (μs)	vantaggi
MWPC	200	$<10^{-5}$	50	10	Tempo di risoluzione
MSGC	30	$<10^{-5}$	20	5	Alto rate, risoluzione spaziale
Cam. drift	100	$<10^{-5}$	500	10	Risoluzione spaziale
Cam. bolle	20	100	10^6	10^4	Analisi ev. complessi
c. streamer	30	10	10^3	10^4	Analisi molte tracce
Cam. flash	1000	10	10^3	10^3	Costano poco
c. scintilla	200	5	10^3	10^4	semplicità
Emulsioni	3	0	∞	10^9	Risoluzione spaziale
Fibre scint.	35	$<10^{-5}$	20	1	Alto rate
Silici	10	$<10^{-5}$	50	1	Alto rate

Bibliografia

Tutti gli argomenti sono comunque trattati anche su:

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Più in dettaglio:

Camere proporzionali e a deriva :

i. Blum & Rolandi, Particle Detection with Drift Chambers, Springer Verlang, 1994
ii. Principle of operation of multiwire proportional and drift chambers F. Sauli su Experimental Tecniques in High Energy Physics, T.Ferbel (editore),World Scientific, 1991

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Silicon Microstrip detectors A. Peisert su Instrumentation in High Energy Physics, F.Sauli (Editore), World Scientific, 1992

Altra bibliografia è indicata sulle slides.